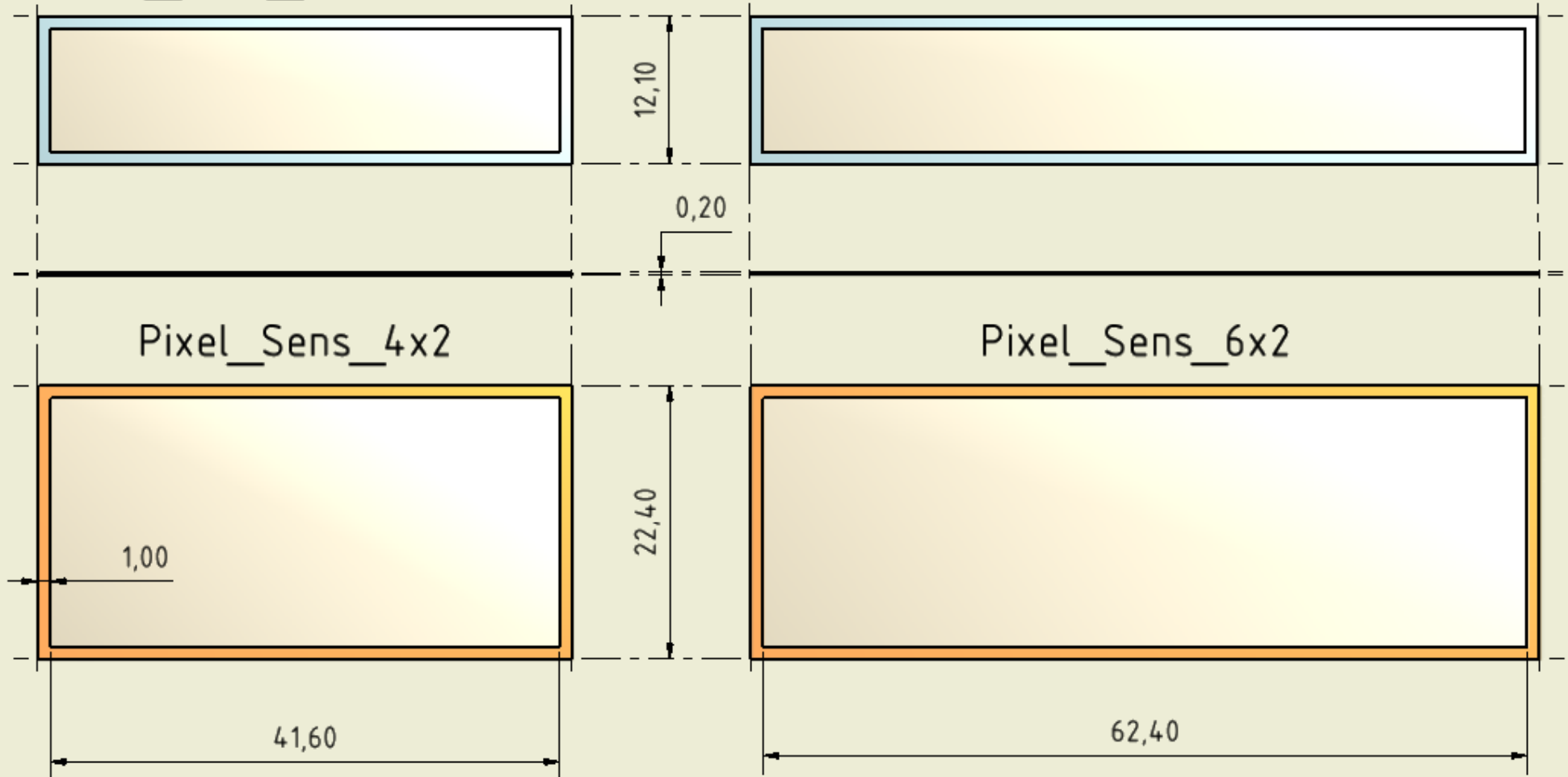
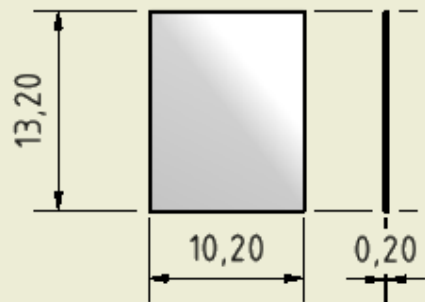


Pixel_Sens_4x1 (2 : 1)

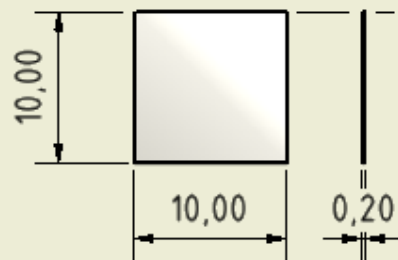
Pixel_Sens_6x1



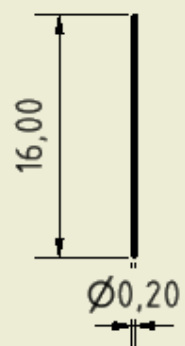
Pixel_Fe (2 : 1)



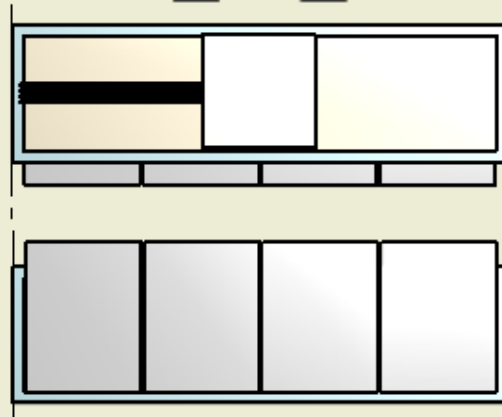
Pixel_Mod_Mc



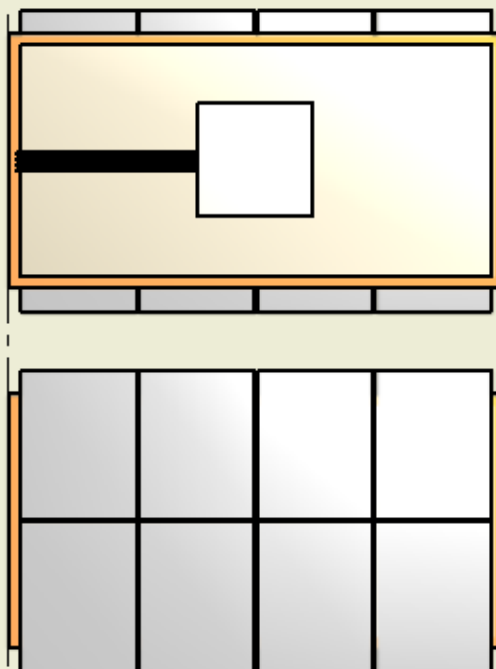
Pixel_Mod_Fiber



Pixel_Mod_4x1 (3:2)



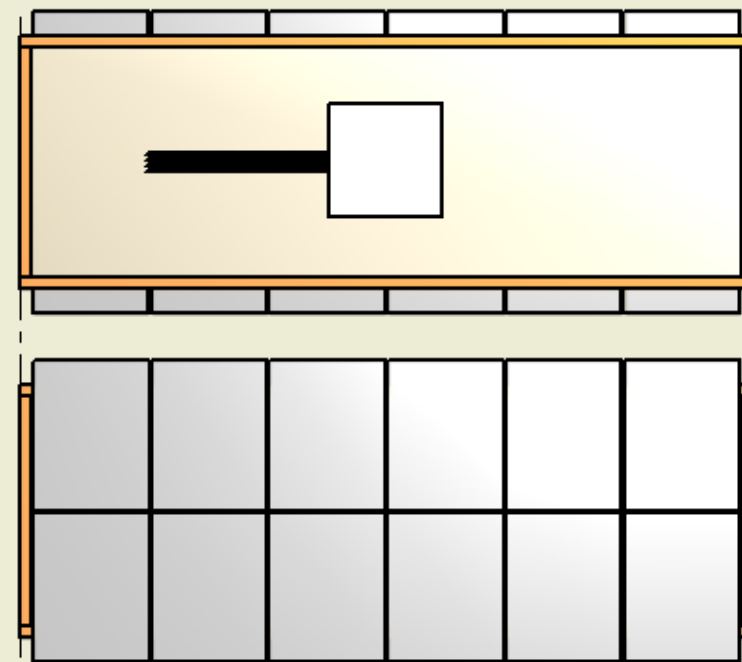
Pixel_Mod_4x2



Pixel_Mod_6x1

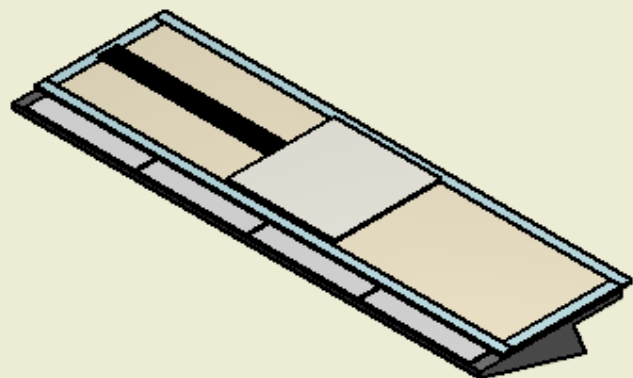


Pixel_Mod_6x2

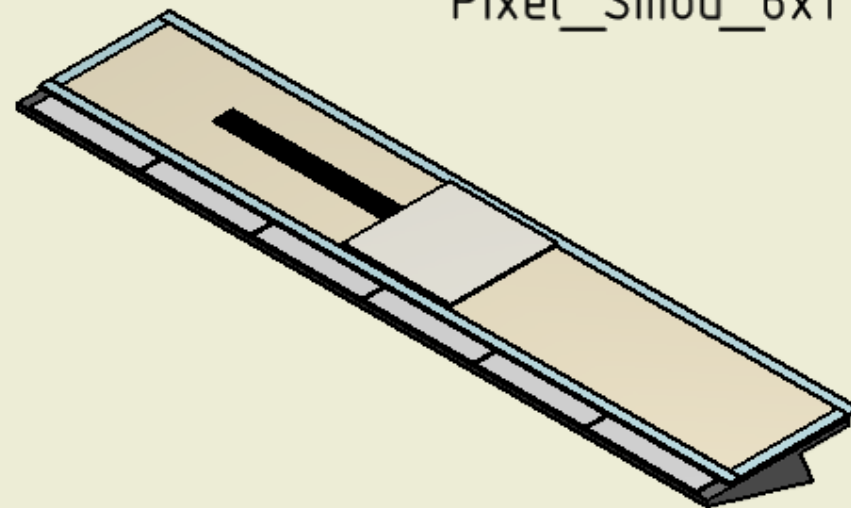


0,60

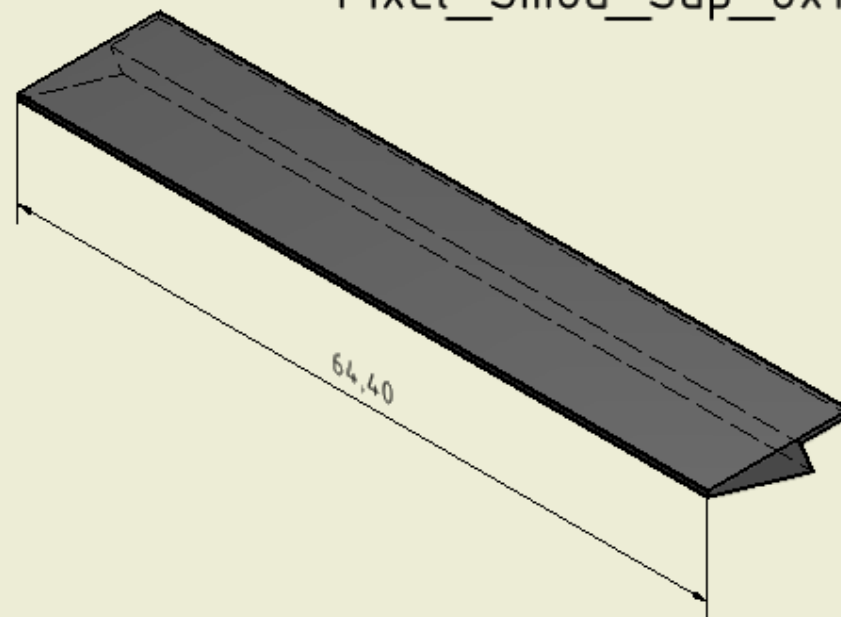
Pixel_Smod_4x1 (2:1)



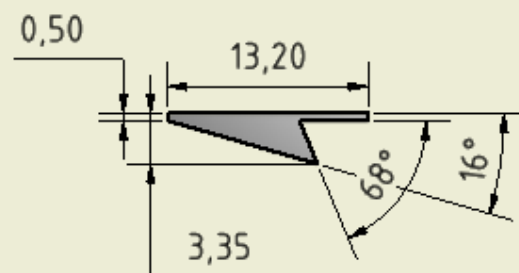
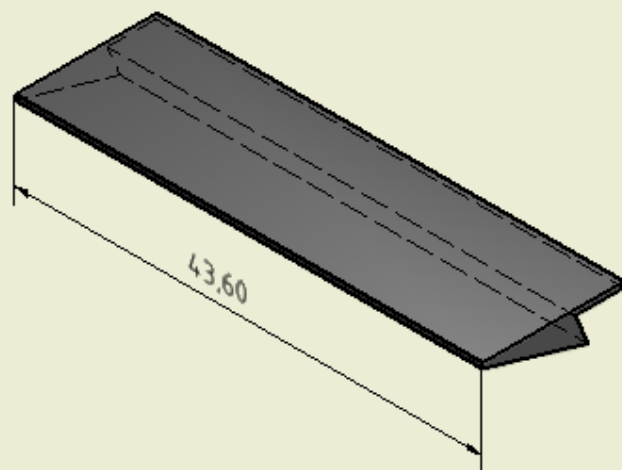
Pixel_Smod_6x1

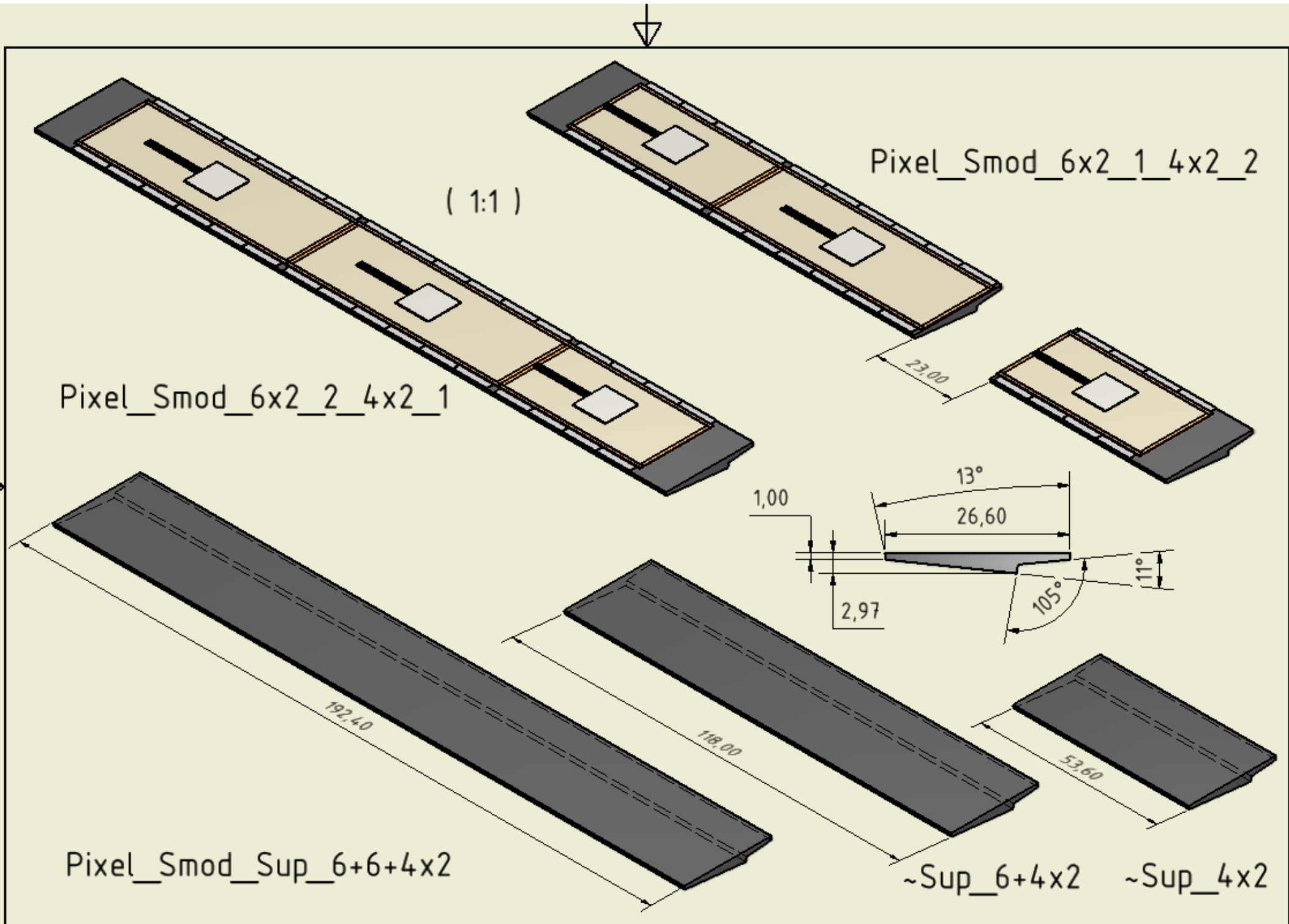


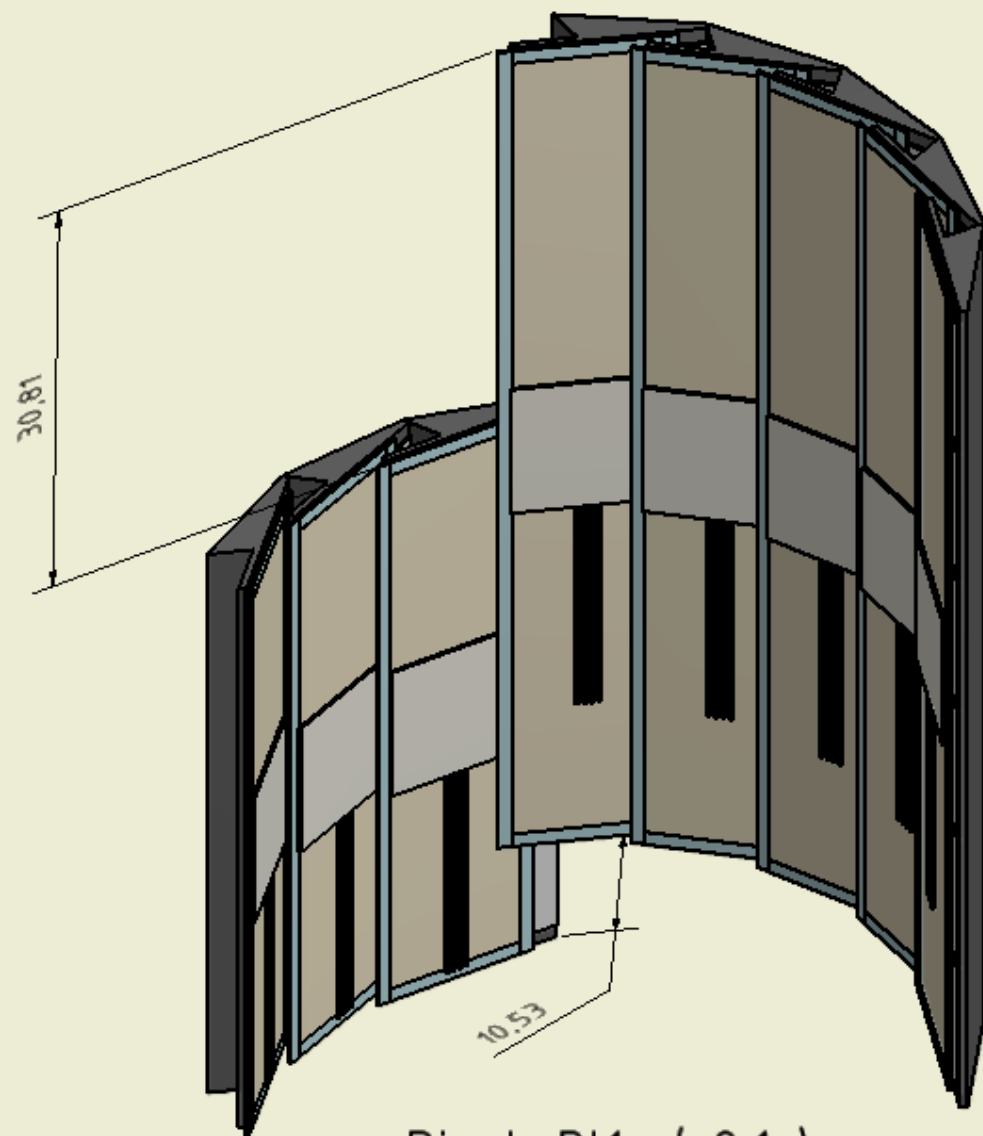
Pixel_Smod_Sup_6x1



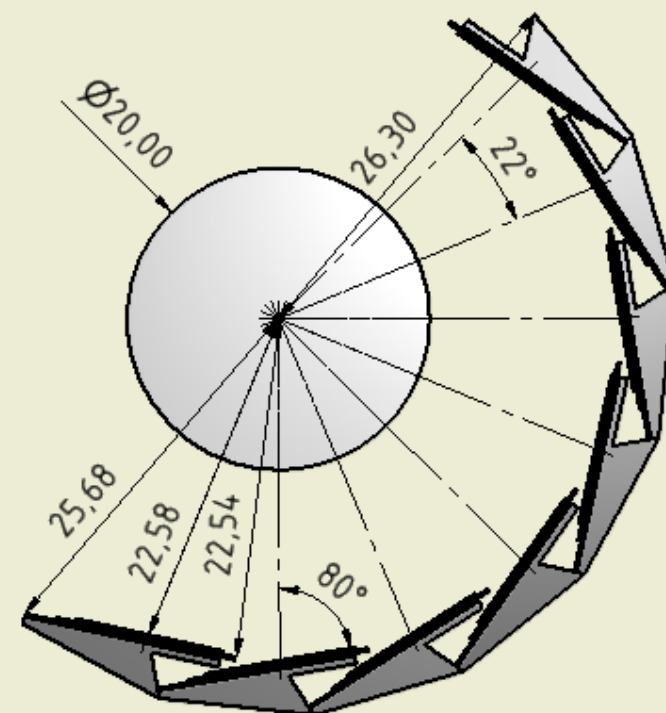
Pixel_Smod_Sup_4x1





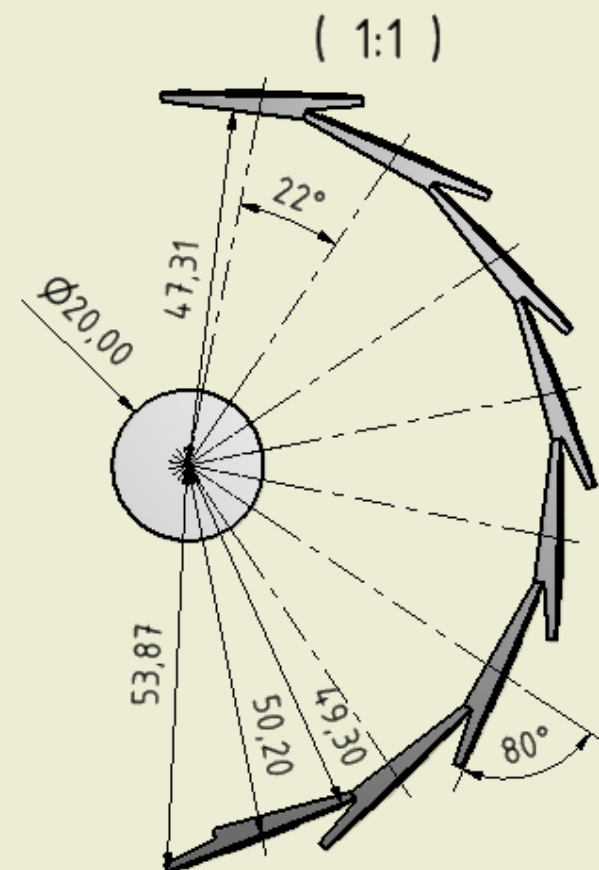
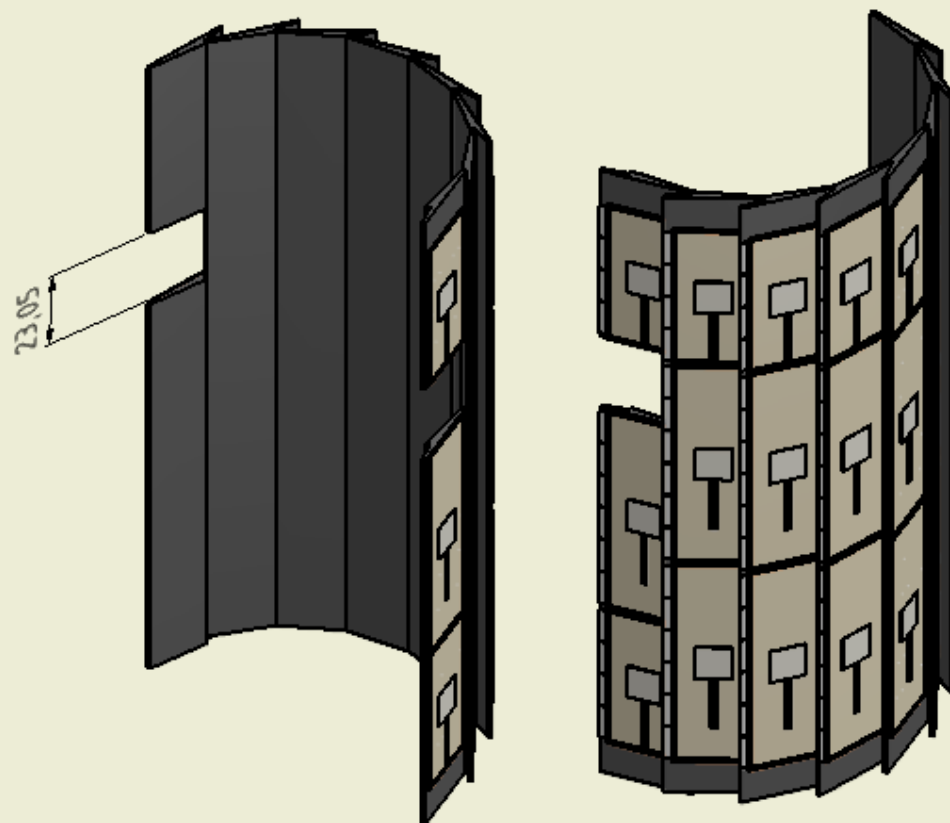


Pixel_Bl1 (2:1)

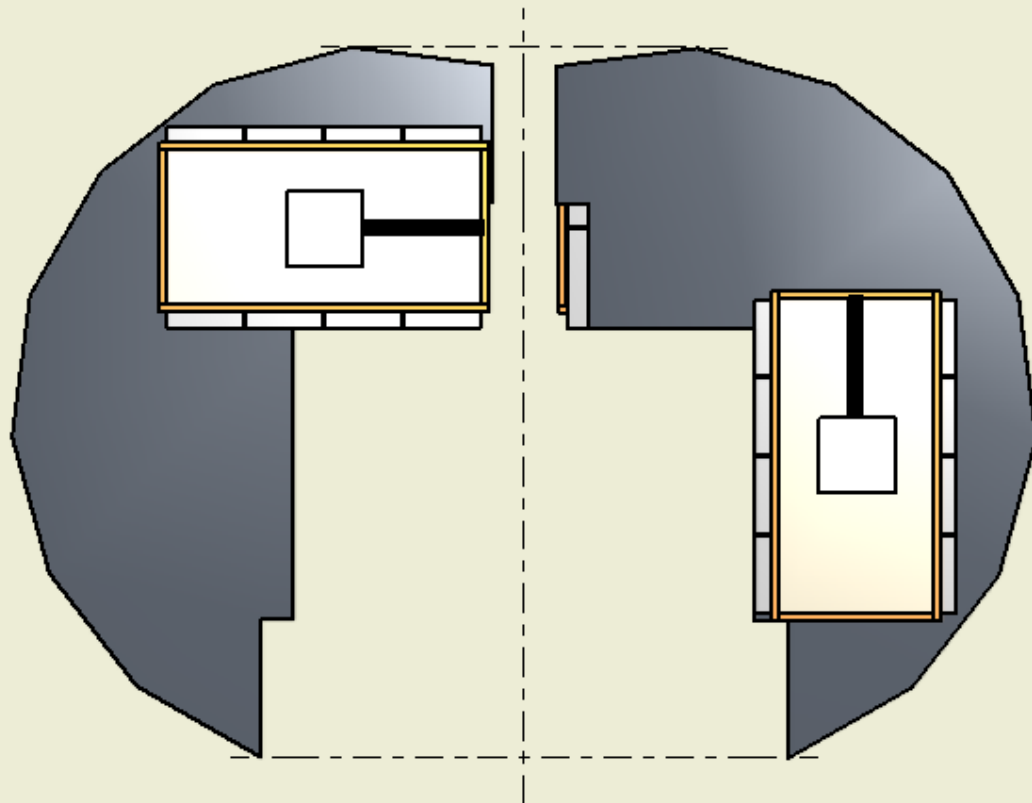


Pixel barrel layer 1

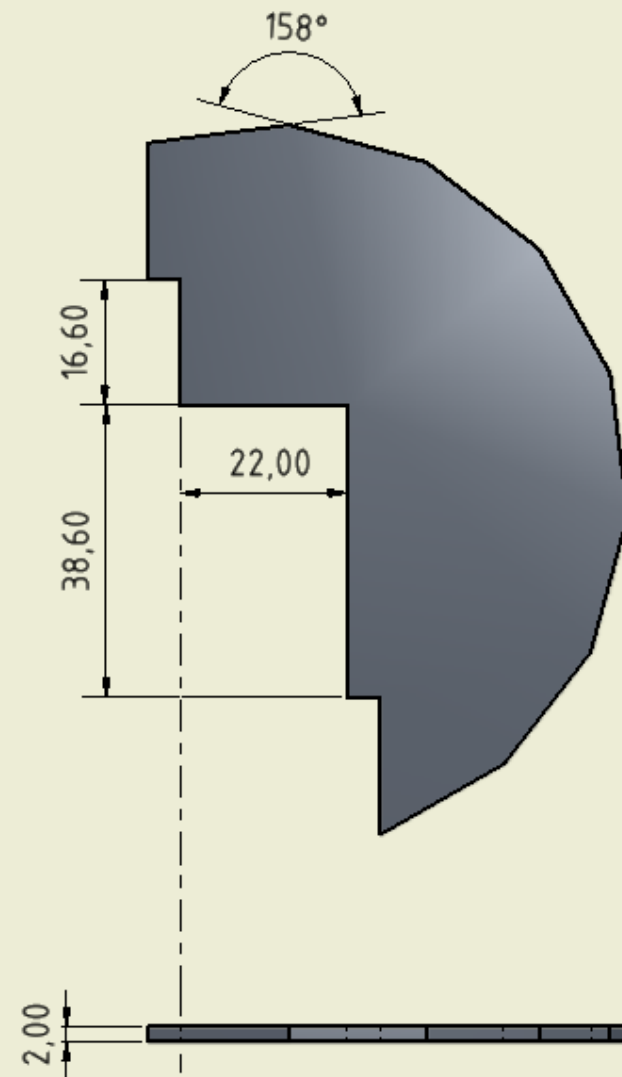
Pixel_Bl2 (1 : 2)



Pixel_Smod_Sdk (1 : 1)

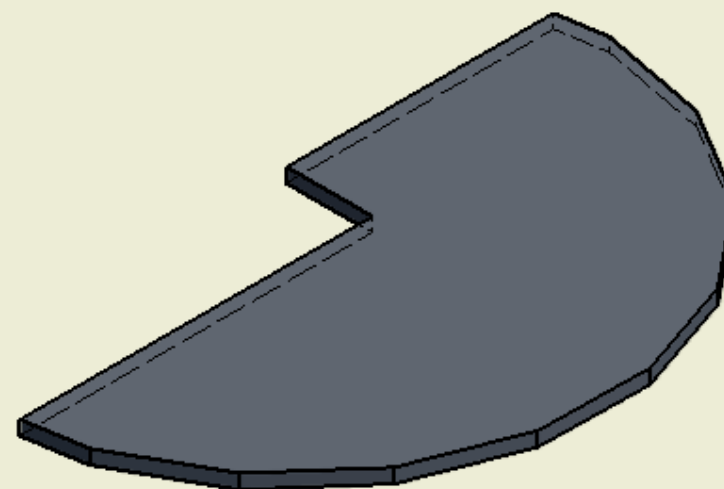
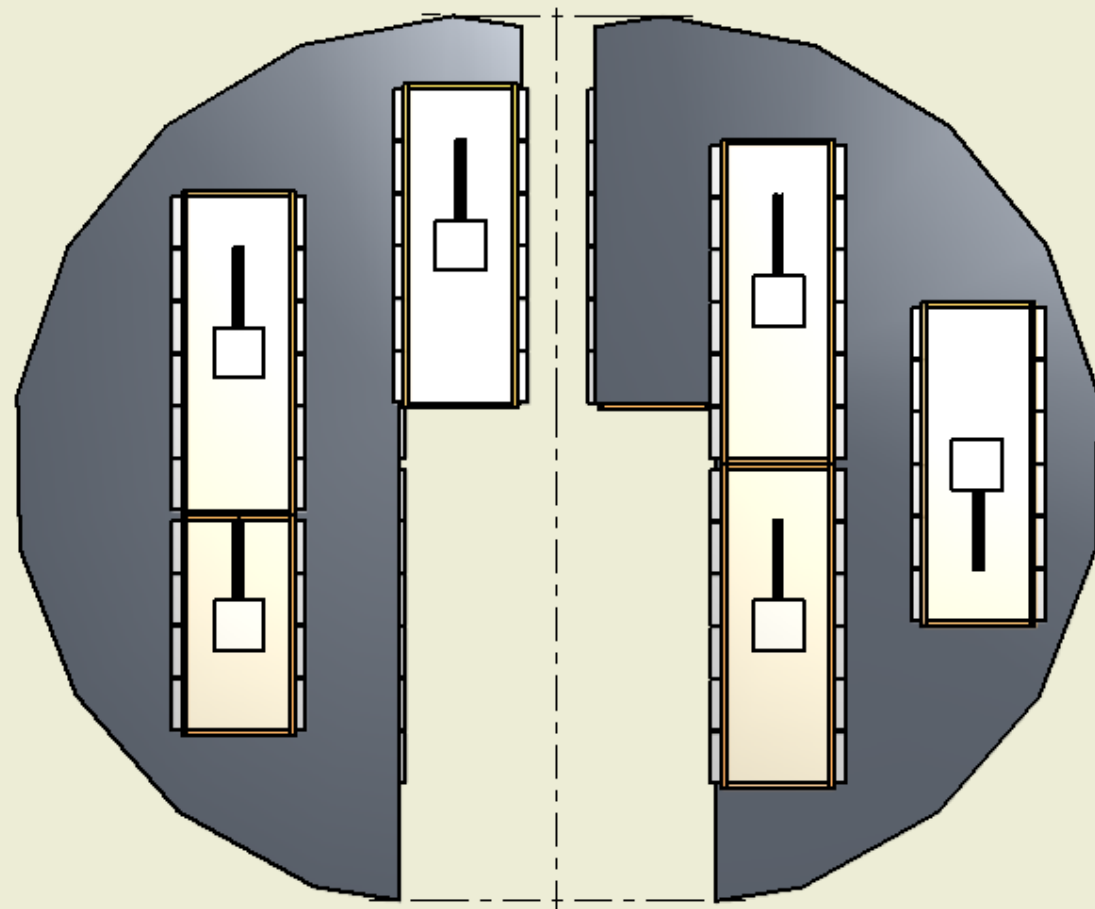


Pixel_Sdk_Sup (1 : 1)

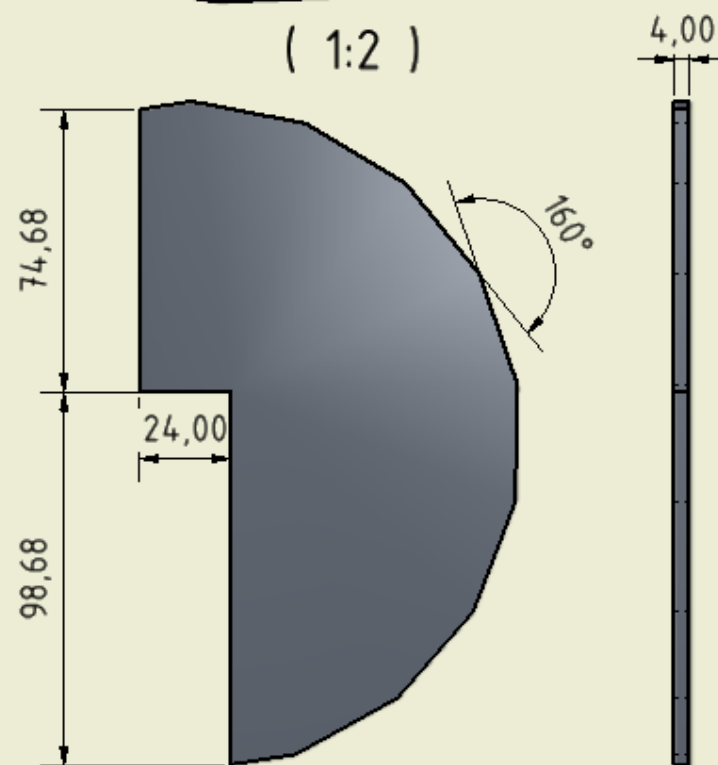


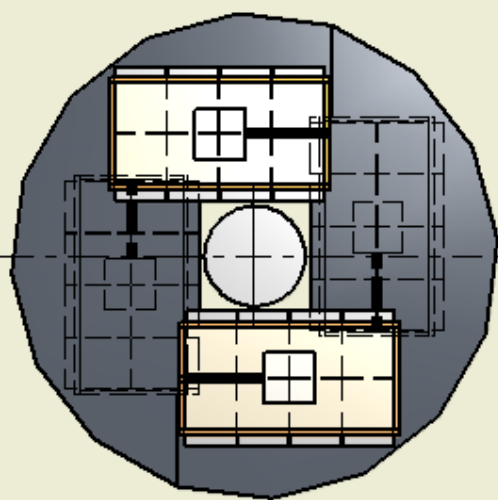
Pixel_Smod_Mdk (2:3)

Pixel_Mdk_Sup (2:3)

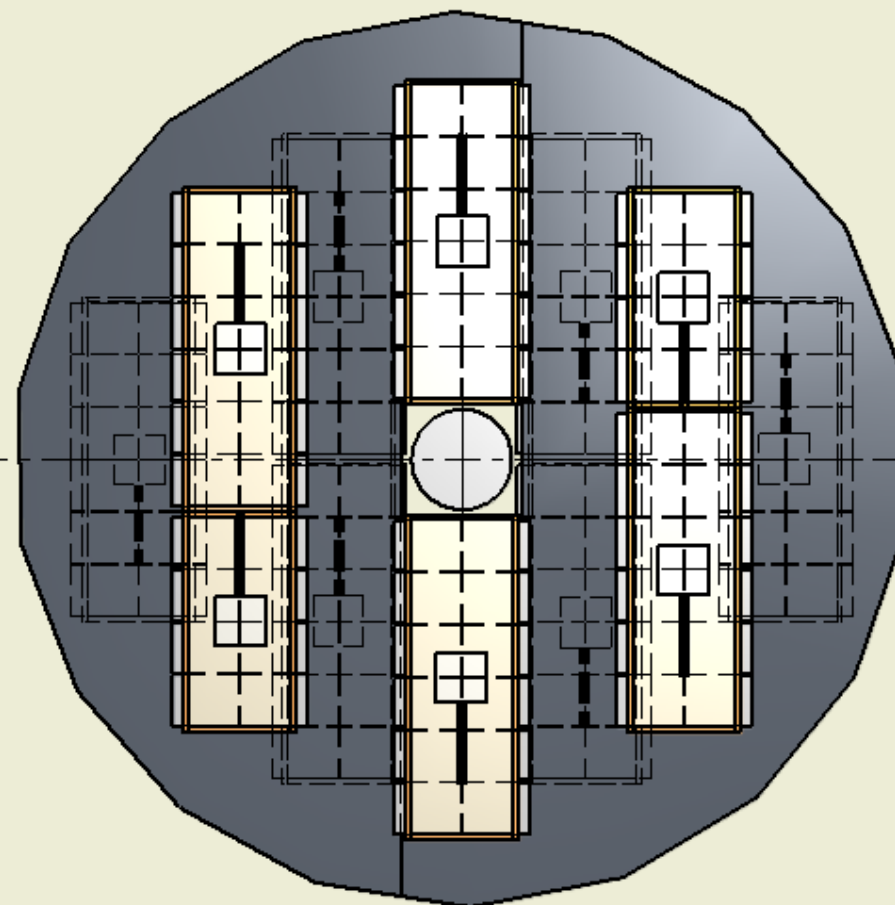


(1:2)





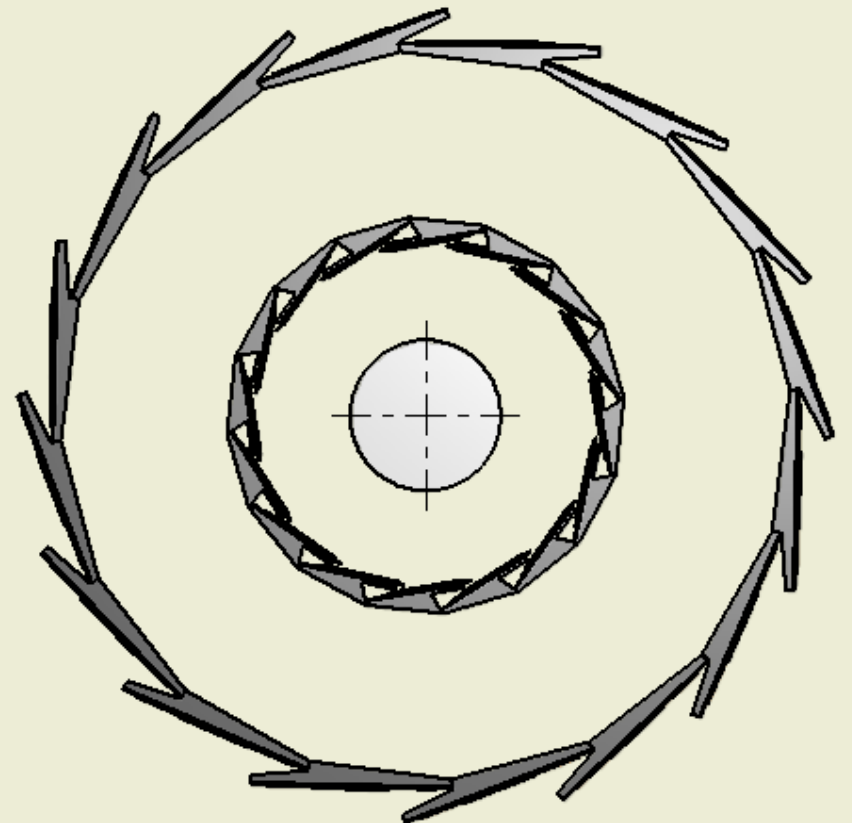
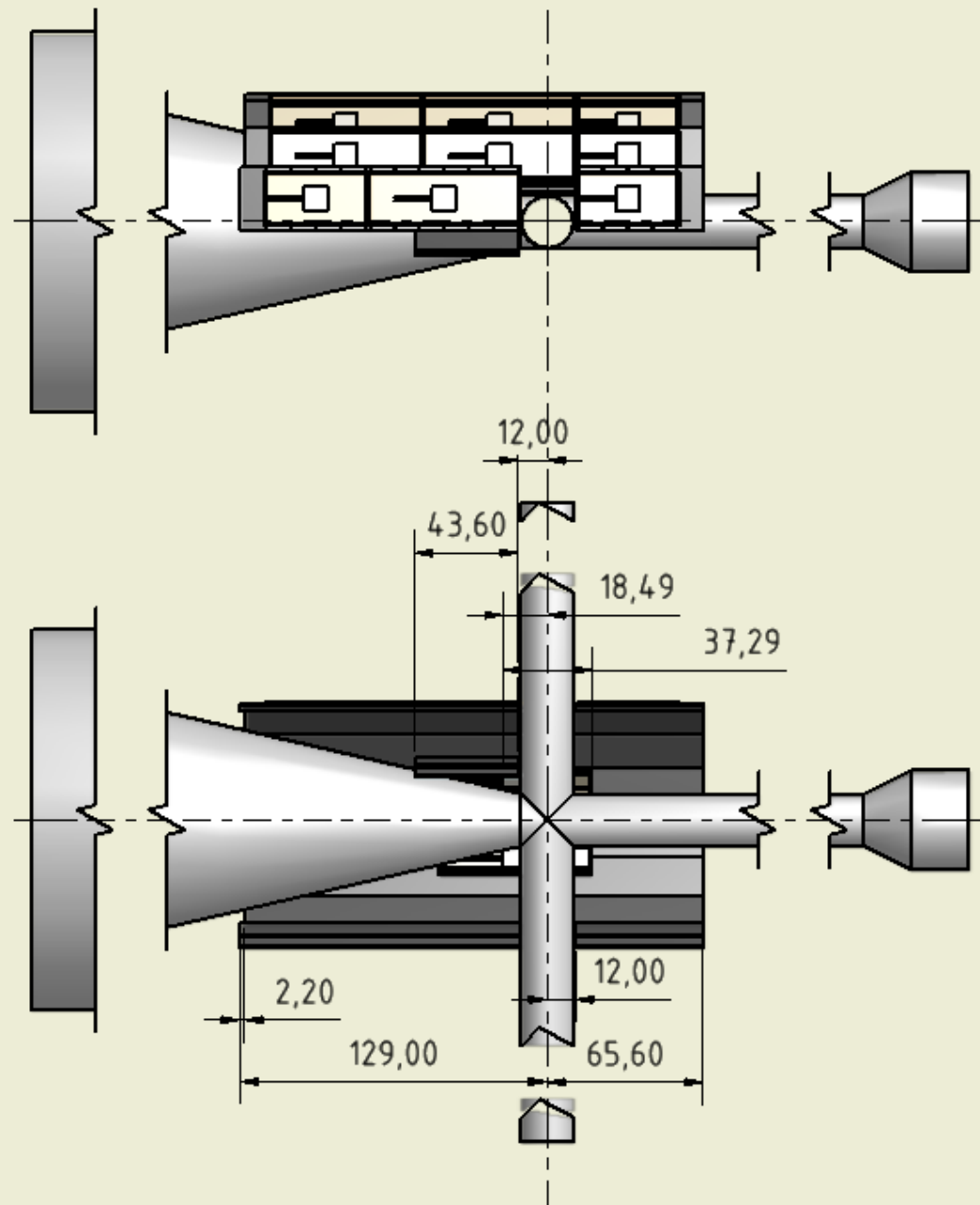
Sdk (both half-disks) (2:3)

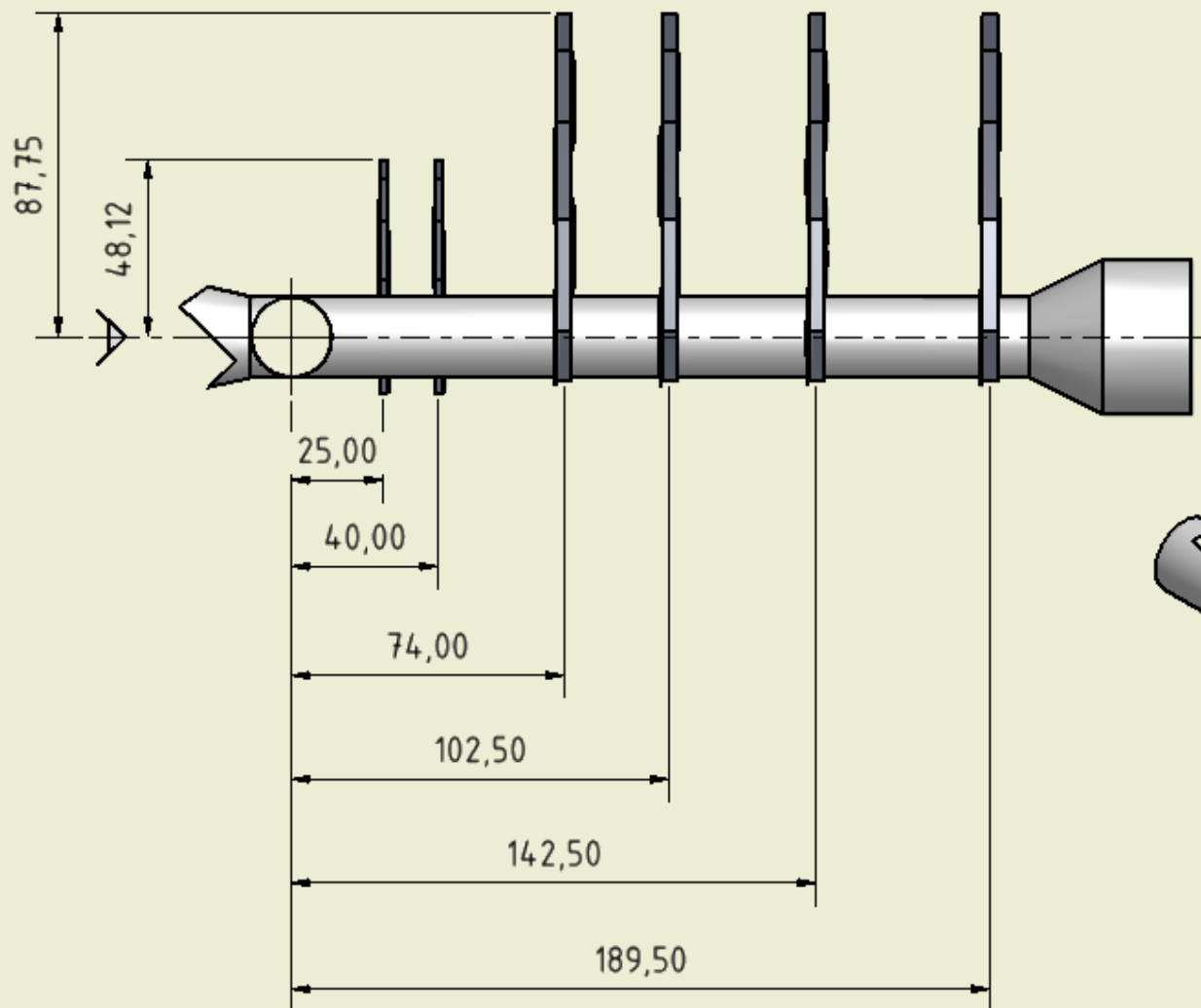


Mdk (both half-disks)

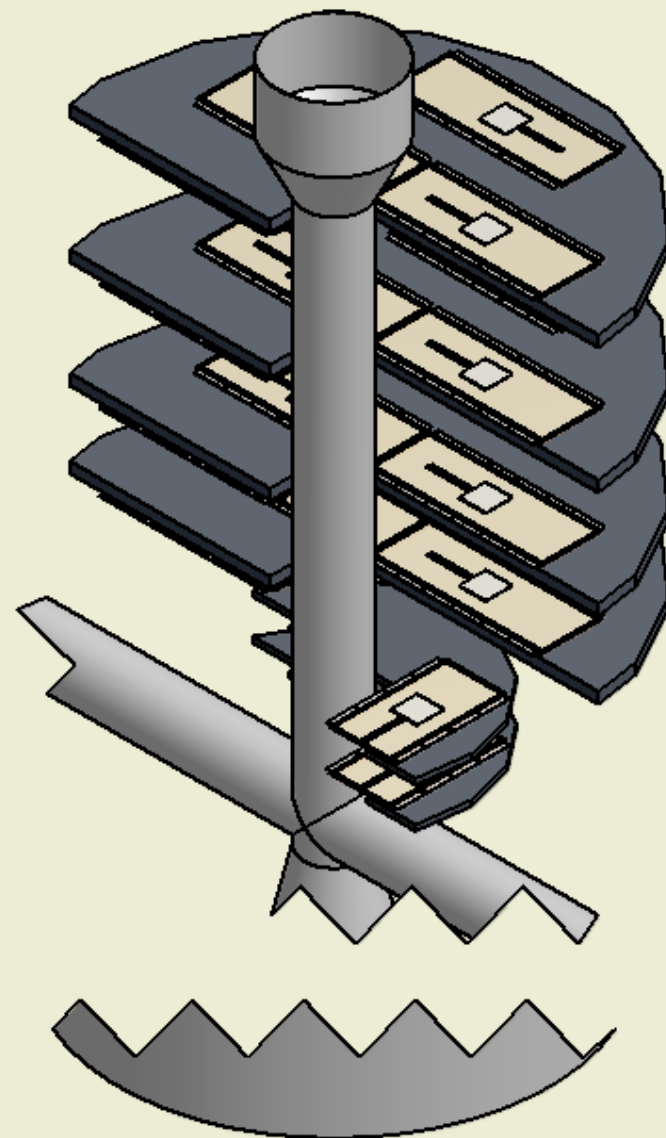
Pixel_Hshell (1:3)

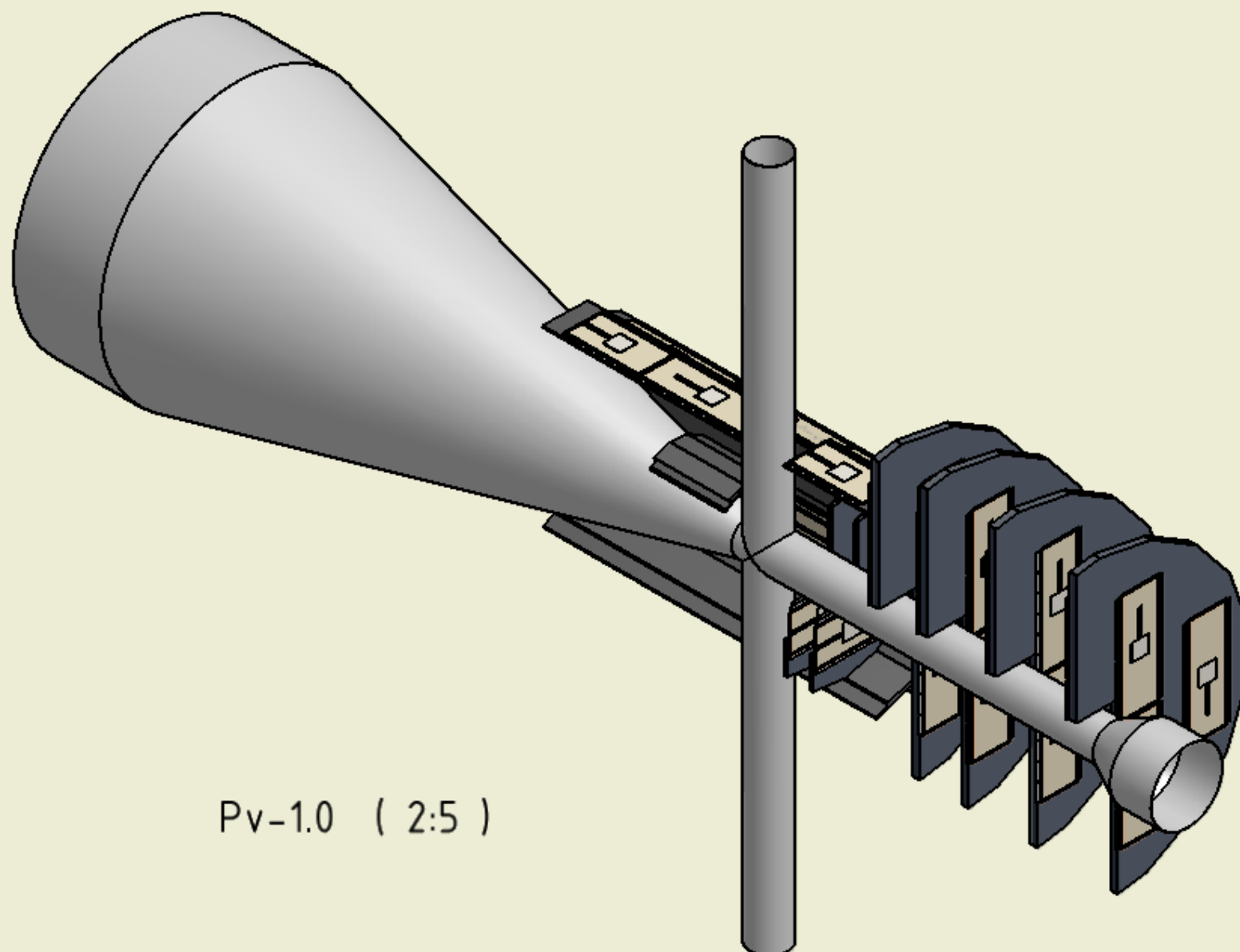
Barrel: radial symmetry (1:1)





Pixel_Fwd (1:2)





Pv-1.0 (2:5)