

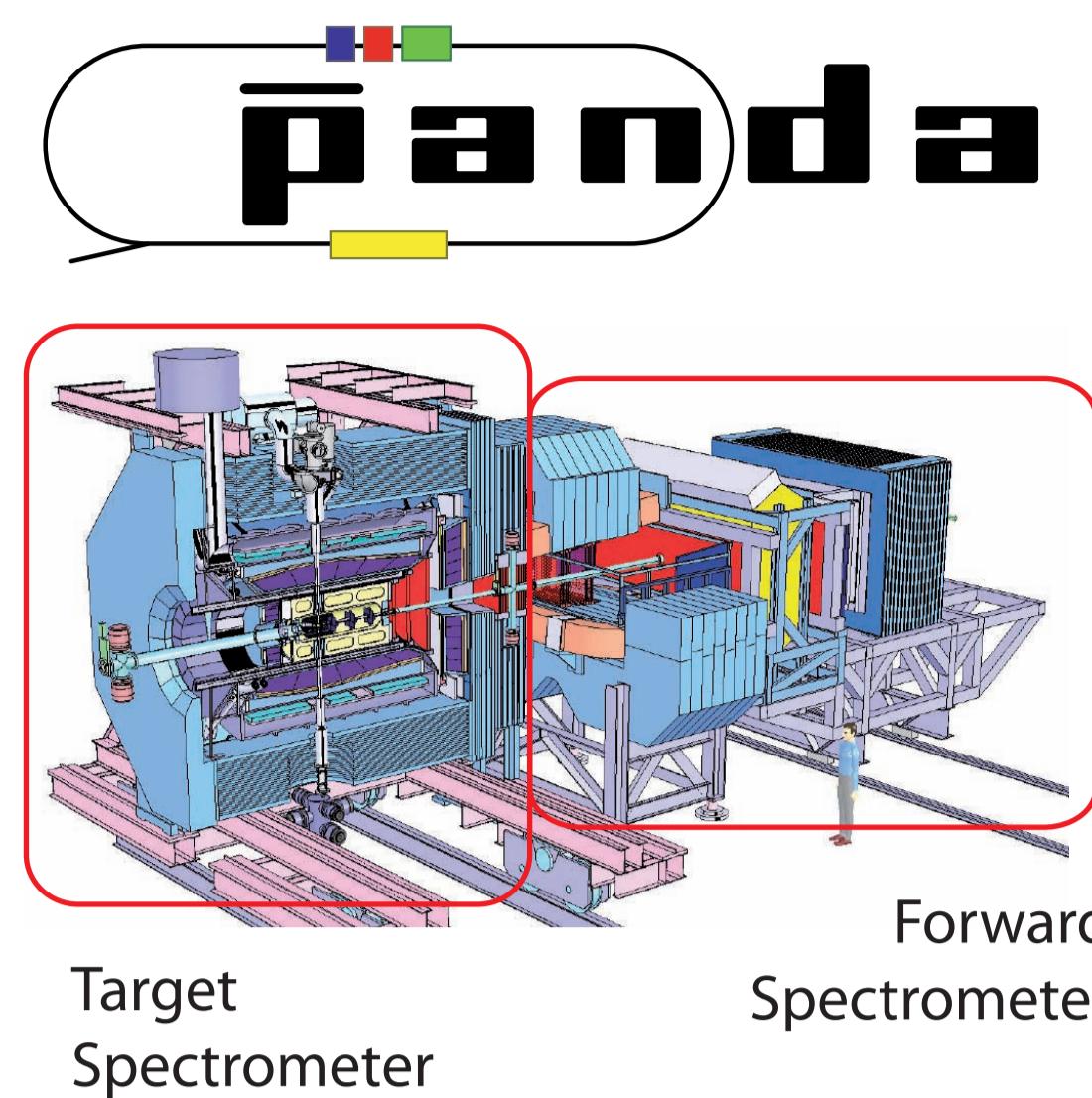
Radiation hardness studies of epitaxial diodes for the PANDA Micro-Vertex-Detector

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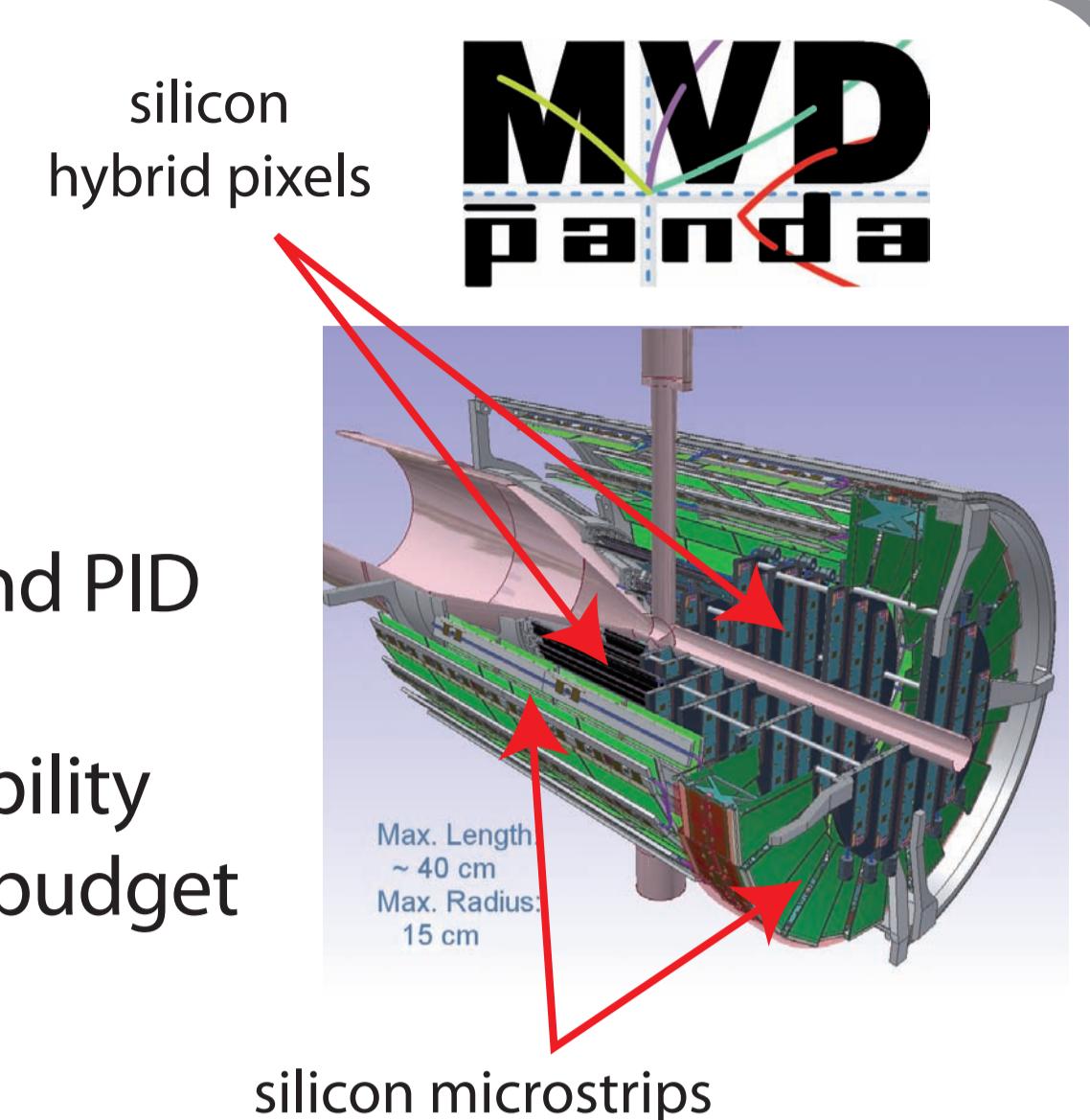
The PANDA Experiment

- Fixed target experiment
- 4π acceptance charged and neutral
- Electron- and stochastic cooling of antiproton beam
- Hydrogen and heavy nuclear targets
- Momenta between 1.5 and 15 GeV/c
- Luminosity of $2 \cdot 10^{32} \text{ cm}^{-2} \text{ s}^{-1}$



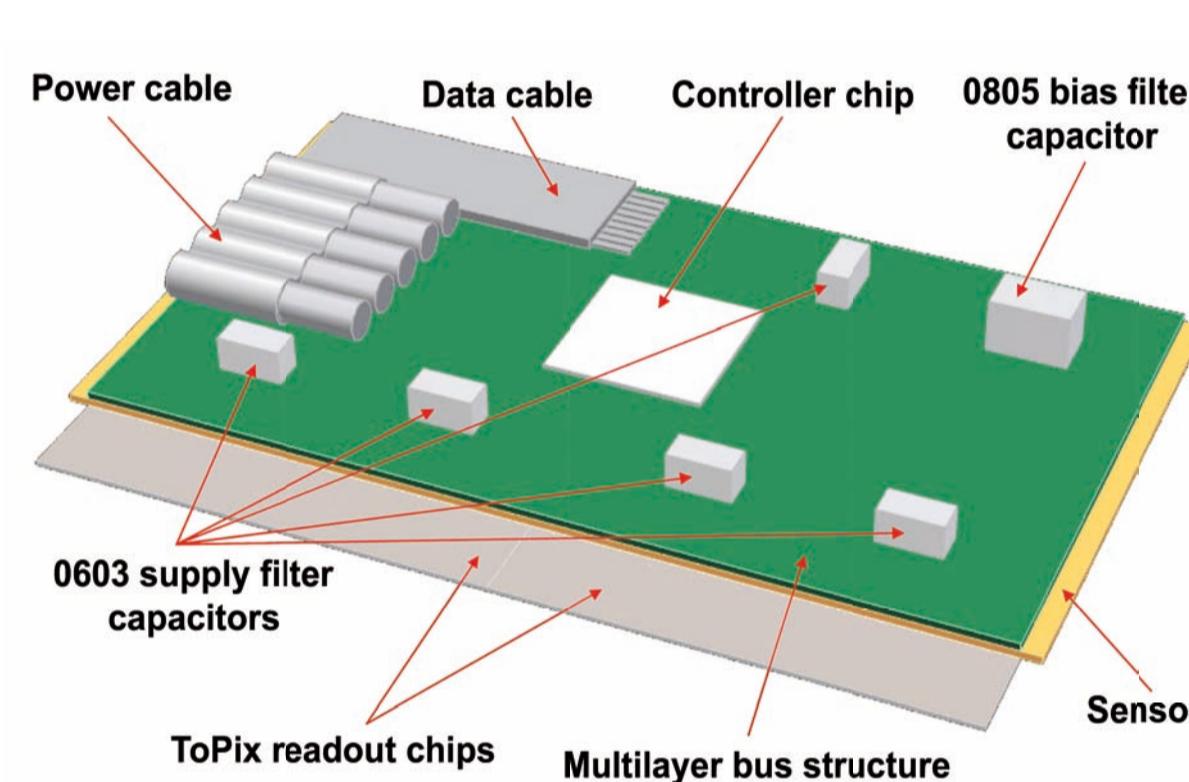
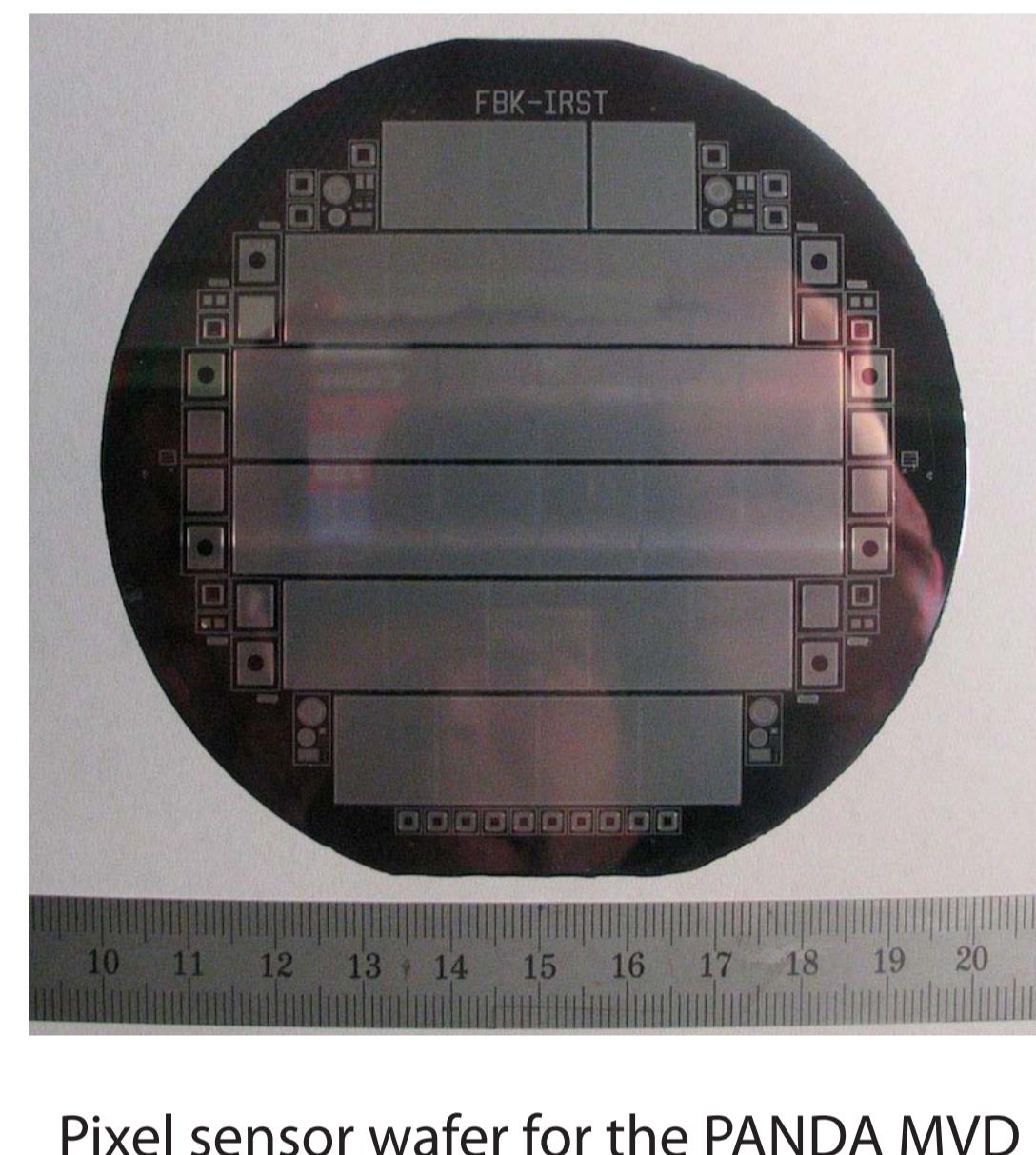
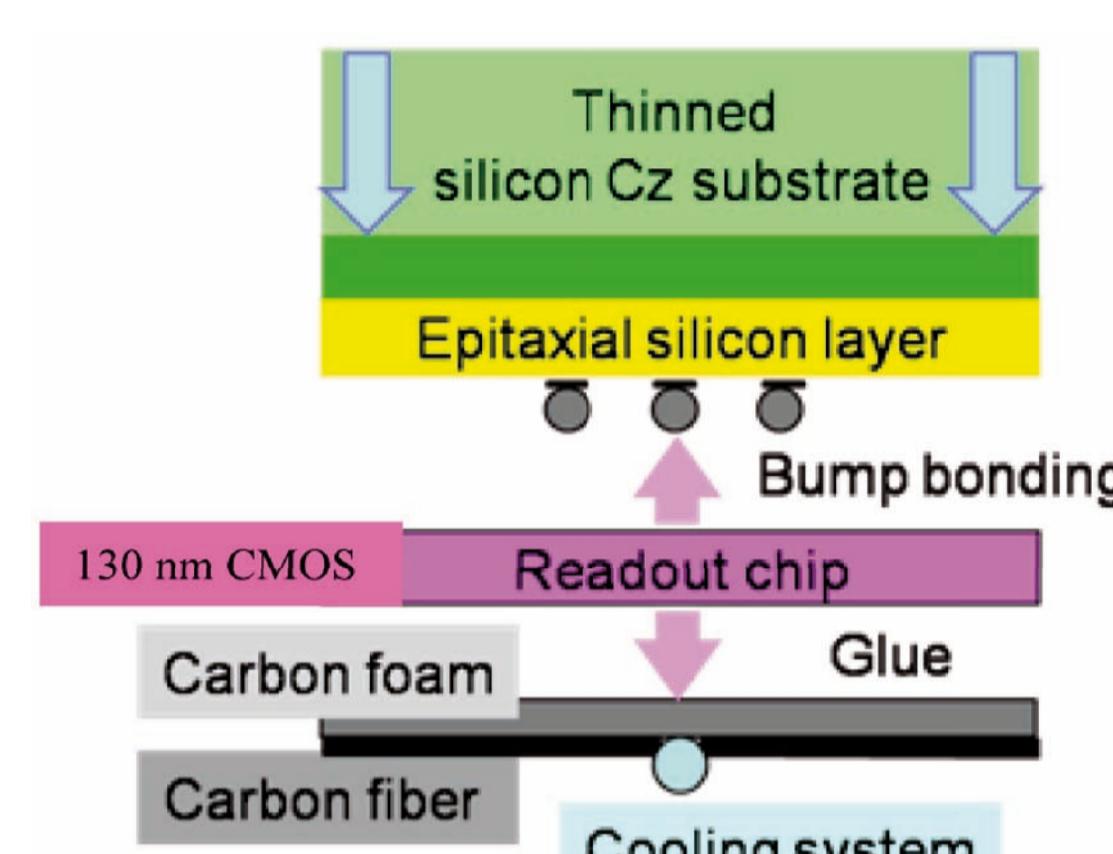
The Micro-Vertex-Detector

- 4 concentric barrels and 6 forward disks
- Reconstruction of primary and secondary vertices
- Improvement of momentum resolution and PID
- Requirements:
 - triggerless readout with high rate capability
 - good time resolution and low material budget
 - high radiation tolerance



Hybrid Pixel Detectors

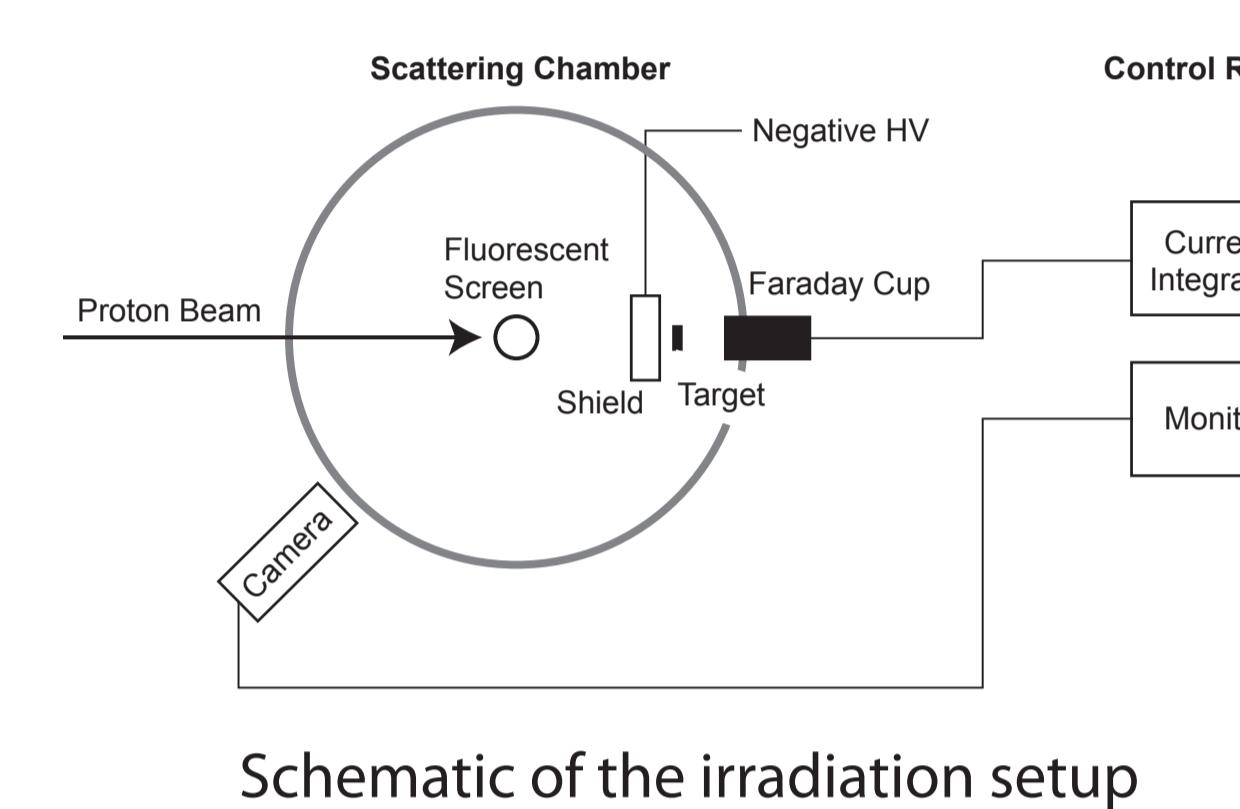
- Developed at INFN, Torino
- Sandwich of sensor, front-end chip and mechanical support (carbon fiber and carbon foam) with embedded cooling system
- Sensor: epitaxial 100 μm silicon layer on a thinned Czochralski (Cz) substrate
- Pixel size 100x100 μm^2
- Front-end connected via bump bonding



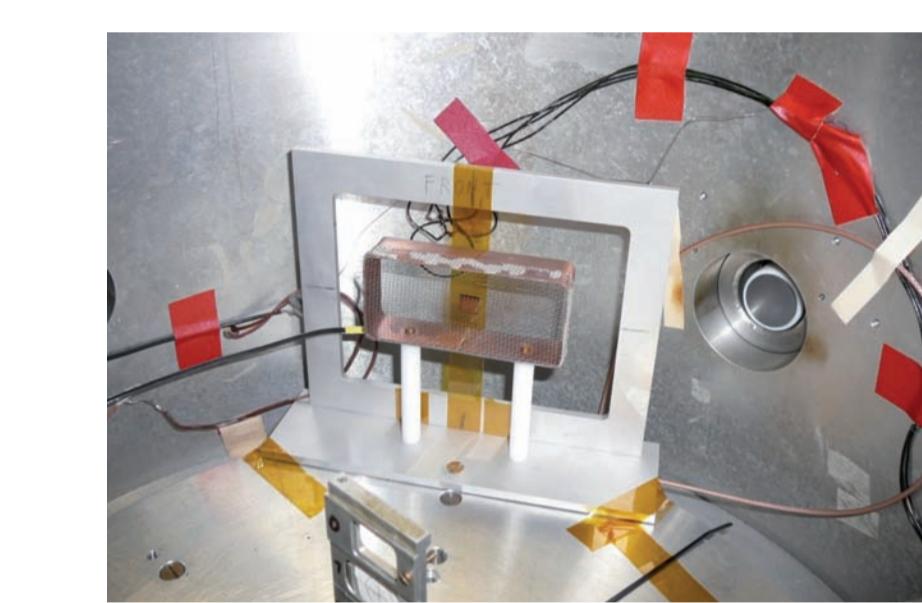
Pixel sensor wafer for the PANDA MVD

Irradiation Setup

- Irradiation of the diodes with a 14 MeV proton beam at the Bonn Isochronous Cyclotron, HISKP
- Also used for studies of PANDA strips: see H.-G. Zaunick, HK 34.2

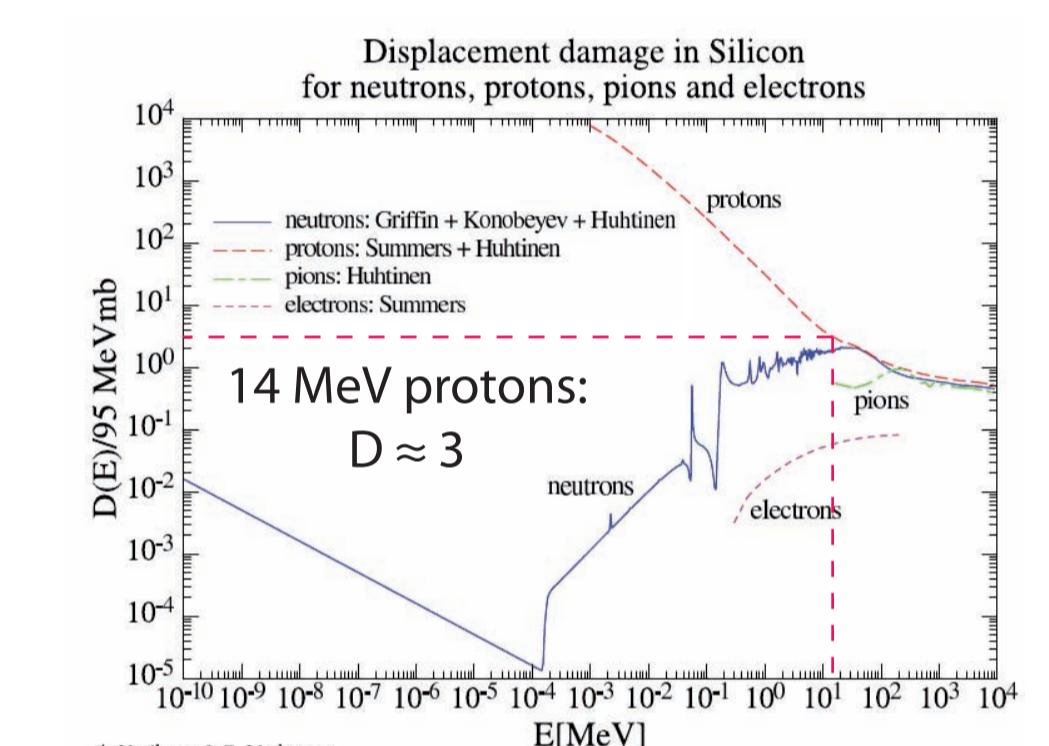


Schematic of the irradiation setup



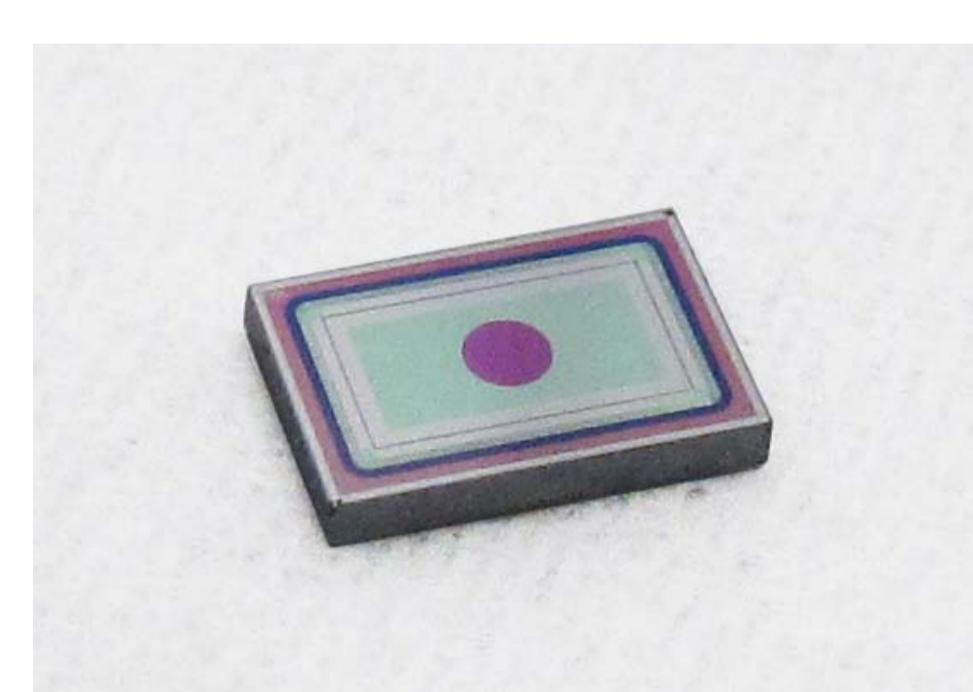
Diode in the irradiation chamber at the cyclotron at HISKP in Bonn

- Equivalent neutron fluence calculated with the NIEL hypothesis
- Two different fluences applied: $5 \cdot 10^{13}$ and $10^{14} \text{ n}_{\text{MeV eq}}/\text{cm}^2$



Prototype Diodes and Measurement Setup

- Wafers substrate and epitaxial layer manufactured at ITME, Warsaw
- Diodes production and oxygenation process performed at FBK, Trento
- Oxygenation process (wafers 2 and 4):
 - 12 hours in O₂ at 1150 °C
 - ~53 hours in N₂ at 1150 °C



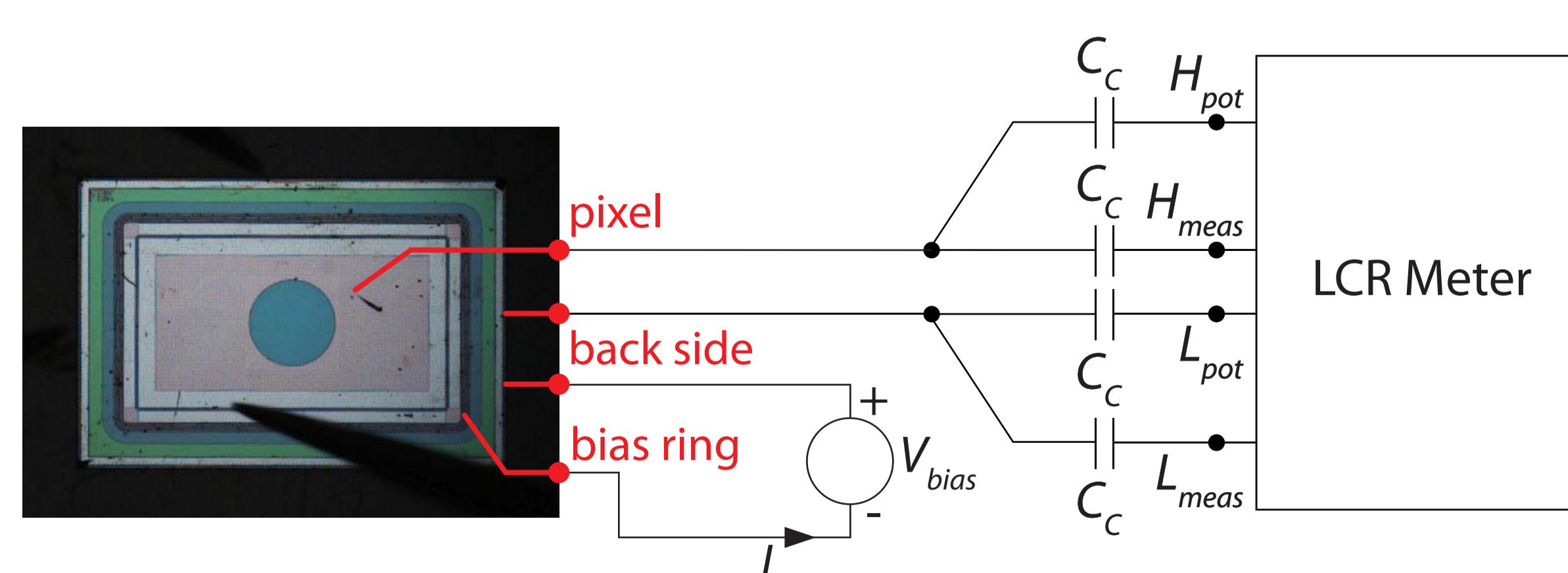
Diode General Properties

Substrate Thickness	$525 \pm 25 \mu\text{m}$
Wafer diameter	$100 \pm 0.5 \text{ mm}$
Substrate resistivity	$0.008 \pm 0.02 \Omega \cdot \text{cm}$
Orientation	<100>
Conductivity type/dopant (substrate)	n+/Sb
Conductivity type/dopant (epitaxial layer)	n/P
Diode size	$3 \times 5 \text{ mm}^2$

Epitaxial Layer Properties

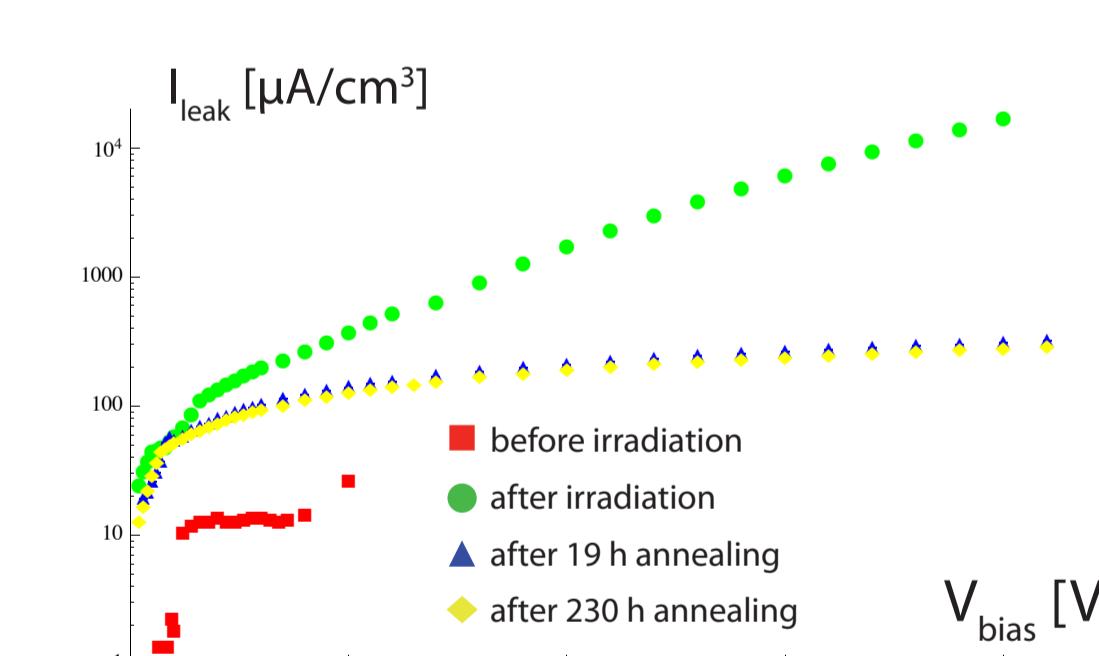
Wafers 2 and 14	Wafers 4 and 15
Thickness	$99.34 \pm 100.38 \mu\text{m}$
Resistivity	$3610 \Omega \cdot \text{cm}$

- I-V and C-V curves of the diodes are measured with a PC-controlled LCR meter and a voltage source
- Diodes are connected with a probe station through a biasing box



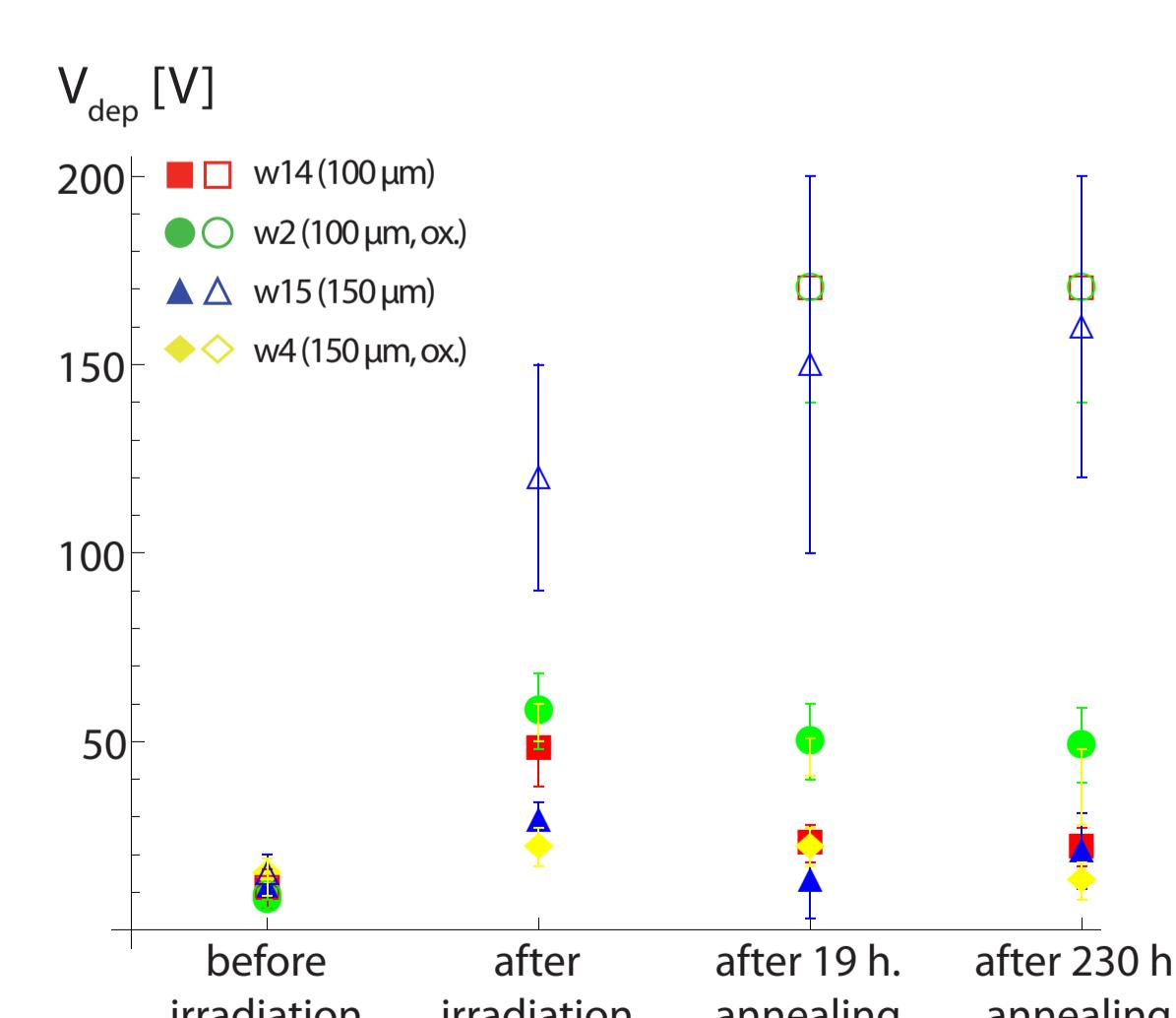
Experimental Results

- Diode parameters measured:
 - before irradiation
 - immediately after irradiation
 - after ~19 hours and after ~230 hours of annealing at 60 °C



I-V curves of the same diode before and after the irradiation. A partial recovery can be observed after a thermal annealing phase

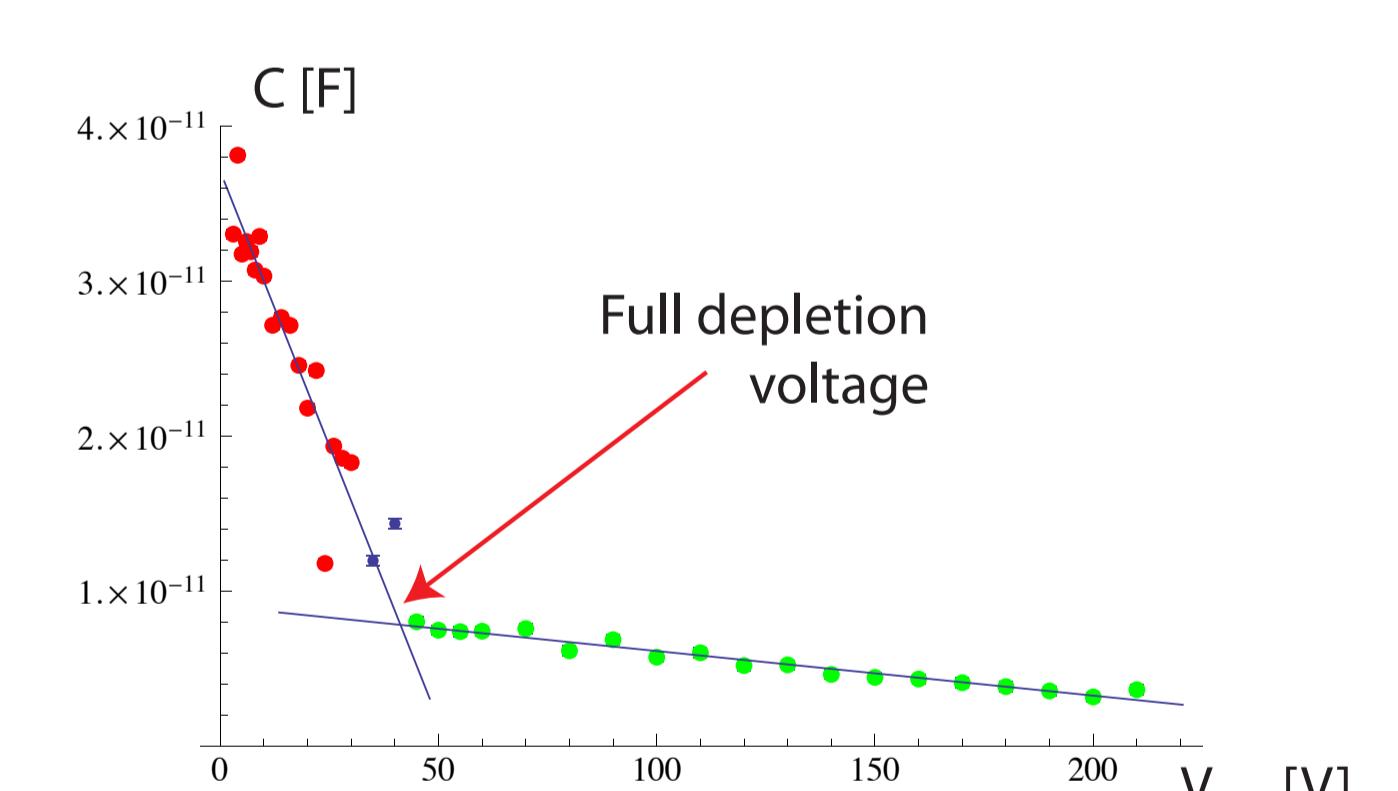
- C-V and I-V curves are used to determine the full depletion voltage and the leakage current, respectively



Annealing behavior of the full depletion voltage and of the leakage current. Filled and empty markers represent low and high fluences, respectively

Pre-irradiation results

Wafer Type	V_{dep} [V]	C [pF]
w14	$100 \mu\text{m}$	11 ± 5
w2	$100 \mu\text{m, ox.}$	9 ± 2
w15	$150 \mu\text{m}$	13 ± 4
w4	$150 \mu\text{m, ox.}$	15 ± 5



Determination of the full depletion voltage from the C-V curve of an irradiated diode

