

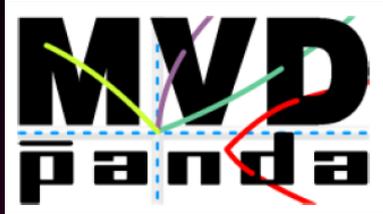


# my TDR topics



## Topics :

- i. on-module electronics
  - F/E chip ( ToPiX )
  - module controller (*if any...*)
- ii. off-module electronics (*still in the detector volume*)
  - optical transceiver
  - power regulator



# On module electronics



## 1. Electronic readout

### 1.1 Requirements

### 1.2 Readout architecture

### 1.3 Front-end ASIC

#### 1.3.1 Pixel cell

#### 1.3.2 ToPiX architecture

### 1.4 Module controller

### 1.5 ASIC prototypes

#### 1.5.1 ToPiX v1 : design and test results

#### 1.5.2 ToPiX v2 : design and test results

#### 1.5.3 ToPiX v3 : design and test results



# Off module electronics



## 1. Optical data transmission

### 1.1. GBT

### 1.2. Optical boards

## 2. Power regulation

### 2.1. DC-DC converter boards



# TDR status



- \* Electronic readout :
  - \* first release 70% complete, to be revised
  - \* module controller and ToPiX v3 test results still missing
- \* Off-module electronics :
  - \* just started
  - \* mostly from CERN developments  
(GBT, DC-DC converters)