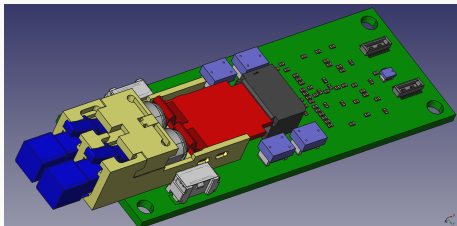
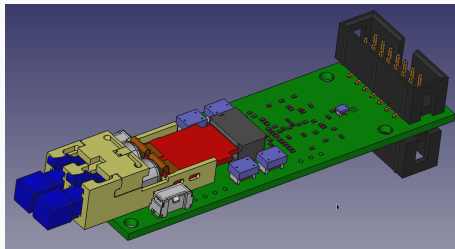


TWO VERSION OF GBTX OPTOELECTRONIC BOARDS

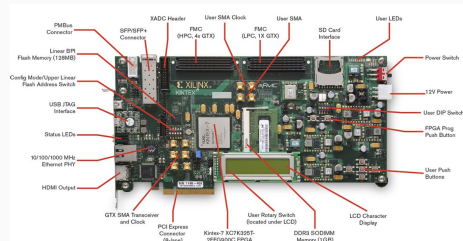
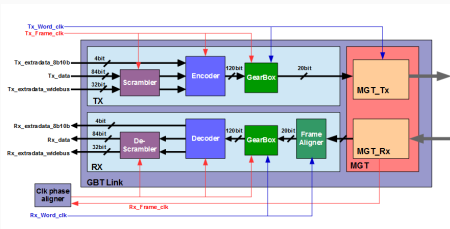


- The board size should be similar to final one.
- The connectors match with those chosen for the Aluminium cables.



- The connectors for the ELink are changed to match with those on the Topix4 board.
- The board design has been submitted for production and is expected for next January.

GBT-FPGA IMPLEMENTATION ON EVALUATION BOARDS



- The GBT-FPGA code from CERN has been implemented on a Virtex6 FPGA.
- The line data rate has been halved to be compatible with the ML605 evaluation board.

- A new evaluation board has been purchased to test the GBT-FPGA code at full speed.
- It is a Kintex7 evaluation board with an optical transceiver running at 10 Gbit/s.