Report for



# Temperature and Humidity Monitoring Board for $\overline{P}ANDA (THM\overline{P})$

(AntiProton Annihilations at Darmstadt)

# Strong Interaction Studies with Antiprotons

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**Cover**: The picture shows the  $\mathsf{THMP}$  mainboard. The dimensions of the mainboard are  $(120 \times 130)$  mm<sup>2</sup>. The eight connectors for the piggyback boards are placed on the left and right side of the PCB. Next to them the 8:1 multiplexers are located. At the front the power and CAN-Bus connectors and the main ICs, the microcontroller and the ADC are mounted.

# Abstract

This document presents the development and tests of the Temperature and Humidity Monitoring Board for  $\overline{P}ANDA$  (THM $\overline{P}$ ).

The THMP has been developed to monitor the temperature and humidity inside the electromagnetic calorimeter (EMC) of the PANDA target spectrometer. Therefore it has 64 channels and is designed to read out PT100 temperature sensors and HIH-4000 humidity sensors. This device has been constructed to operate in a temperature range from -30 °C to +30 °C. To cover the whole temperature range of the EMC, the measurement range will also be from -30 °C to +30 °C to +30 °C with a resolution of 0.05 K. The relative humidity (RH) can be measured in the range from 0 to 100% RH with an accuracy of 3.5%. For the read-out of this device a CAN interface is foreseen.

The  $\mathsf{THMP}$  should fulfill the environmental requirements of operating in the cooled area of the EMC. Different tests were carried out to verify this.

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# 1 Motivation

At -25 °C, the light yield of PWO depends heavily on the temperature with a change of 3%/K. Therefore, the temperature of the crystals has to be stabilized and monitored to ensure the temporal and spacial homogeneity of the calorimeter as well as to diagnose and control the longitudinal temperature distribution along one crystal. When setting an upper limit for the impairment of the energy resolution  $\sigma_{E, \text{temporal}} \leq 0.2\%$  caused by *temporal* variation of the crystal temperatures, one obtains a requirement for the temporal homogeneity of the calorimeter:

$$\sigma_T = \frac{0.2\%}{3\%/\text{K}} \approx 0.07 \,\text{K}$$
, (1.1)

which leads to a demand of a temporal homogeneity of about 0.05 K.

Adding to the temporally induced decrease in energy resolution, one also has to take into account the spacial inhomogeneities. Simulations show a maximum allowance for the temperature gradient along a crystal of 0.01 K/mm [Zh09].

Another quite important point is the humidity inside the calorimeter. If ice forms on the crystal's surface total reflection is no longer provided and therefore the light yield decreases. This is why one has to flush the whole calorimeter with dry nitrogen and monitor the relative humidity inside. For monitoring the temperature and humidity, the  $\mathsf{THMP}$  (Temperature and Humidity Monitoring Board for PANDA) was developed.

# 2 Temperature and Humidity Sensors

### 2.1 Development of ultra-thin Platinum Temperature Sensors

The space in one of the four  $2 \times 2$ -subcompartments of a  $4 \times 4$ -subunit is very limited, and it is even more reduced due to a further stabilizing carbon fiber cross and the DF2000MA mirror foil, which envelopes each crystal. Furthermore, the option is discussed to wrap each  $2 \times 2$  crystal package in a metallized and grounded foil for improved shielding of RF noise.

To be still able to check the temperature in the vicinity of the crystals, a very thin sensor has to be developed. The space available is  $80 \,\mu\text{m}$  at maximum, and the temperature resolution has to be in the order of 0.05 K. Prototypes for such sensors have been developed, ref. [Sc09] and are being presented on the following pages of this report.

#### 2.1.1 Design of the Temperature Sensors

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The idea is to measure the resistance change in a thin platinum wire to express the temperature change. Platinum is used for two reasons: first, it has a rather high specific resistance ( $\rho = 110 \cdot 10^{-9} \,\Omega$ m), a fact which reduces the needed wire length in contrast to a material with lower specific resistance. Second, the large temperature coefficient of the resistance change of  $3.88 \times 10^{-3} \,\mathrm{K}^{-1}$  leads to well measurable resistance changes, while the temperature only shifts marginally.

The measurement is to take place via a four-terminal sensing, which is presented in the next section. This method allows that the resistance of the supply wires to the point where the sensor starts be ignored. This is a common method to ensure high-precision resistance measurements.

The platinum wire is brought onto a self-adhesive Kapton film and covered with a non-adhesive film on the reverse side. Up to now, such highly specialized sensors are not available on the market, so they have to be developed by the  $\overline{P}ANDA$  Collaboration.

The choice of wire diameters is quite relevant. On the one hand, a thin wire has the advantage of saving wire length while having the same resistance. In addition, the height of the sensor is reduced by using a thin wire. On the other hand, a thin platinum wire tends to tear at only small tensions, so that the wire has to be applied with special precaution. It has been investigated that a 25  $\mu$ m wire is a fair compromise.

The resistance of the sensor at 0 °C is called  $R_0$  and is chosen to be 100  $\Omega$ . This is to provide comparability to commercial PT100-sensors and to obtain measurable voltages with a testing current of only 1 mA. With the above values for  $\rho$ ,  $R_0$  and  $d = 25 \,\mu$ m, the result is:

$$R_0 = \rho \cdot \frac{\ell}{A} = \rho \cdot \frac{\ell}{\pi \cdot r^2}$$

$$\Rightarrow \ell = \frac{R_0 \cdot \pi \cdot r^2}{110 \times 10^{-9} \,\Omega\mathrm{m}} \approx 0.45 \,\mathrm{m}$$
(2.1)

Thus, one has to apply 50 cm of platinum wire on the Kapton film quite compactly. Small deviations in the order of centimeters are no problem, because each sensor has to be singly calibrated anyway. A similar wire length is desirable to allow a similar load of the current sources as well as noise filtering (more on this subject in subsequent chapters).

In principle, a platinum wire passed by an electric current is nothing else than a small ohmic heater. At -25 °C we obtain for the dissipated power a value of

$$P = R \cdot I^{2} \\ \approx 90 \,\Omega \cdot \left(1 \cdot 10^{-3} \,\mathrm{A}\right)^{2} = 0.09 \,\mathrm{mW}$$
(2.2)

According to [Kl], the error due to self-heating of a 100  $\Omega$  sensor at a current of 1 mA is 0.004 K; so with equation (2.2) we get a temperature change of about 0.0036 K at a temperature of -25 °C.



Figure 2.1: Different designs of the platinum sensors

Therefore, the rather small testing current of 1 mA seems to be well chosen and the influence of self-heating needs not be taken into account.

Different sensor designs have been developed to fathom the possibilities of constructing them (fig. 2.1). All these sensors have a thickness not noticeably larger than 60 microns at their active area (the active area is the area which is inhibited by the platinum wire between the two four-wire contacts and therefore the only material that contributes to the sensor's resistance), so that it is possible to place the sensors on the long sides of the crystals. Their width is designated uniformly to 20 mm, which is based on the dimensions of the crystals' long sides.

The length of a sensor is open to trial and error. The simplest design is one that combines only few windings and long, straight paths. With such a design, it is possible to average the temperature of a crystal's long side with straight paths in approximately the length of a crystal. Looking at figure 2.1, such a sensor is number 06. Glued on it, four long platinum wire paths with a length of 190 mm each, form the resistance. Both ends of the paths are connected with two copper wire loops, which are led to the sensor's end and connected to thicker wires for testing purposes.

#### 2.1.2 Read-out of the Temperature Sensors

As already mentioned, the temperature sensors will be read out via a four-terminal sensing. With this method four cables are attached to the sensor. Two wires provide the constant supply current of  $I_{\text{ref}} = 1$  mA while one measures a voltage  $U_{\vartheta}$  at the other two cables. The voltage is thereby in accordance with Ohm's law

$$U_{\vartheta} = I_{\rm ref} \cdot R_{\vartheta} \qquad , \tag{2.3}$$

whereby  $R_{\vartheta}$  is the resistance of the sensor. Figure 2.2 illustrates this method.

The four-terminal sensing allows to neglect the resistance of the wires so that one only measures the resistance of the sensor.

Within the range below 0 °C the resistance  $R_{\vartheta}$  depends on the temperature as follows:

$$R_{\vartheta} = R_0 \left( 1 + a \cdot T + b \cdot T^2 + c \cdot (T - 100 \,^{\circ}\mathrm{C}) \cdot T^3 \right) \qquad (2.4)$$

For commercial PT100 the coefficients of this equation are as follows:  $a = 3.91 \cdot 10^{-3} / ^{\circ}\text{C}$ ,  $b = -5.78 \cdot 10^{-7} / ^{\circ}\text{C}^2$  and  $c = -4.18 \cdot 10^{-12} / ^{\circ}\text{C}^4$ . To reach the aspired resolution of 0.05 K the self-constructed temperature sensors must be calibrated to evaluate the above-named coefficients. By



Figure 2.2: Principle of the four-terminal sensing [Ti02]

means of the two equations (2.3) and (2.4) one can obtain the temperature T of the environment. For this method it is important that the drift of the current is less than the resolution, which has to be measured.

### 2.2 The Humidity Sensors

For the humidity monitoring, the HIH-4000 series from Honeywell [Ho08] is selected. This sensor is a laser trimmed, thermoset polymer capacitive sensing element with an on-chip integrated signal conditioning. Thus, the output voltage of this sensor is proportional to the relative humidity (RH) and ranges from 0.8 V to 3.8 V.



Figure 2.3: HIH-4000 series. Pins from left to right: GND, OUT and  $V_{CC}$ 

The sensing element's multilayer construction provides a resistance to most application hazards such as wetting, dust, dirt, oils and common environmental chemicals. With a supply voltage of +5 V DC and a typical current draw of only 200  $\mu$ A, which results in 1 mW dissipated power, the HIH-4000 series is suitable for the cooled area inside the EMC. Moreover, this sensor is quite small. Without the pins, the dimensions of this sensor are only (4.17 × 8.59 × 2.03) mm<sup>3</sup>. Another advantage is the sensor's operating range, which goes down to  $-40 \,^{\circ}$ C and 0% RH with a resonable accuracy of 3.5% RH [Ho08].

Other humidity sensors were tested but this type provides the best accuracy and performance. A precise description with several studies of the sensor's behavior can be found in ref. [Fr09].

# 3 Design of the THM $\overline{P}$

### 3.1 Environmental Requirements

If the THMP is placed inside the cooled area of the forward endcap, it has to fulfill the following conditions: First of all the THMP must work inside a -25 °C environment without any variation from working at room temperature (RT). Second, it has to work properly in a magnetic field. According to fig. 3.1 the field strength behind the mountplate is 1.3 T. For the Barrel part of the EMC the field strength is even 2.5 T.



Figure 3.1: Magnetic flux density distribution of the  $\overline{P}ANDA$  Solenoid (cross section for half of the horizontal plane where the beam comes from the left). The iron yoke, coil and cryostat, the barrel DIRC and the outer tracker are indicated by their outlines [PA09]

Third, the  $\mathsf{THMP}$  has to be radiation hard. Simulations have shown that the  $\mathsf{THMP}$  will be exposed to up to 10 mGy/h if placed behind the mountplate and the innermost crystals of the forward endcap [Ro09]. This means that in 10 years the  $\mathsf{THMP}$  will be exposed to a dose of up to 440 Gy. The PCB should at least withstand this dose without showing any significant defects due to radiation damage.

# 3.2 Basic Design of the THM $\overline{P}$

The Temperature and Humidity Monitoring Board for  $\overline{\mathsf{P}}\mathsf{ANDA}$  is designed to read out 64 channels. As described in chapter 2, the sensors differ in their supply needs and in their processing of data. In order to be flexible with the number of temperature and humidity sensors, the  $\mathsf{THMP}$  is conceived as a mainboard with connectors to eight piggyback boards. Eight sensors are connected to each piggyback board.

With this design further types of piggypack boards can be produced to monitor other signals.

Figure 3.2 shows a simplified block diagram of the  $\mathsf{THMP}$ . On the mainboard an ADC and a microcontroller (MC) are attached. The data from the  $\mathsf{THMP}$  is transmitted via a CAN bus to the slow control.



Figure 3.2: Block diagram of the  $\mathsf{THMP}$ 

The temperature piggyback board is composed of current sources for the sensors and amplifiers. Due to the low current of 1 mA and the resistance of the PT100, the output voltage of these sensors varies between nearly 88.2 mV and 111.7 mV for a temperature range of -30 °C to 30 °C. However, the ADC on the mainboard has a range of 0 to 4 V. This is why the voltage is amplified. For the HIH-4000 humidity sensors only a +5 V DC supply is needed. Due to the output voltage range of these sensors no amplification is necessary.

The overall dimensions of the THMP are  $(120 \times 130 \times 14)$  mm<sup>3</sup>. A steel case reducing RF noise and facilitating mounting in the EMC will be developed.

### 3.3 Common Parts

The board itself is supplied with  $\pm 6$  V DC. On the boards (mainboard as well as piggyback boards) low dropout voltage regulators provide the voltages needed by the ICs (c.f. fig. 3.3).



Figure 3.3: Operating circuit of the voltage regulators for the positive (a) and negative (b) voltage regulators. For the  $\mathsf{THMP}$  +5 V, +3.3 V, and -5 V regulators will be used.

Altogether, three different voltages are required for the components of the THM $\overline{P}$ . The mainboard needs a +5 V and a +3.3 V supply, the humidity piggyback board only +5 V and the temperature board needs +5 V, +3.3 V and -5 V. For the power supply and data transfer S-ATA connectors are used. The board-to-board connectors with 64 pins are from the FTE and CLE series by Samtec.

SATA power		SATA data		board-to-board	
1 - 3	DGND	1, 2, 6, 7	N.C.	odd pin number	AGND
4 - 6	+6 V	3	CAN L	6,60	AGND
7 - 9	AGND	4	DGND	2, 4	+6 V
10 - 12	-6 V	5	CAN H	62,  64	-6 V
13 - 15	AGND			12, 18, 24, 30, 42, 48, 54	signals
				others	N.C.

Table 3.1: Pin assignment of the connectors

### 3.4 Mainboard

#### 3.4.1 Multiplexer and Filter

Since the  $\mathsf{THMP}$  has 64 channels, but the ADC just 8, the signals have to be multiplexed. Moreover, the signals have to be filtered to reduce the noise. Thus a 8:1 multiplexer MAX4581 [Ma07b] is placed next to each board-to-board connector to connect one of the eight channels of each piggyback board to the ADC. Using the microcontroller, the appropriate channels can be set up. Between the multiplexers and the ADC, 3rd order active low pass filters, such as shown in figure 3.4, are placed.



Figure 3.4: 3rd order active low pass filter [La94]

Due to the magnetic field one cannot use a passive filter with an order higher than one, because it needs inductors. On the one hand, a test measurement with a frequency synthesizer showed that a 1st order passive filter is insufficient for noise reduction. On the other hand, one wants to have as few active parts as possible to keep the heat production of the whole  $\mathsf{THMP}$  as low as possible. If one considers that the number of operational amplifiers normally corresponds to the order of a standard active filter, the filter presented in above figure seems to be a good compromise.

It must be pointed out that  $R_4$  works as a potential divider and thus causes an amplification of the signal. For this reason this resistor is not mounted in the filters on the THMP. The other values are chosen in such a way that the cut-off-frequency of the filter is 15 Hz.

#### 3.4.2 The Analog-to-Digital Converter

To convert the analog signals from the sensors the 14-bit ADC MAX1148 from Maxim Integrated Products, Inc. is used. The MAX1148 is a low-power, 8-channel ADC which operates from a single +4.75 V to +5.25 V supply. All analog inputs are software configurable for unipolar or bipolar and single-ended or differential operation. This ADC has an internal reference of  $V_{\text{ref,i}} = 4.096 \text{ V}$ and a dynamic range of  $0 \, V \dots V_{ref}$ . The MAX1148 is built in a 20-pin TSSOP package and has a 4-wire serial interface available, which directly connects to SPI devices. It provides two clock modes to perform the analog-to-digital conversions: the internal clock mode and an external serial-interface clock respectively [Ma07a]. For SPI one has to take account of the correct clock polarity and sampling edge in the SPI control registers. To start the conversion a control byte has to be sent via the SPI (tab. 3.2). Therefore one requires three 8-bit transfers to perform a conversion. One to configure the ADC and two more to clock out the 14-bit conversion result. For the  $\mathsf{THMP}$  the internal clock mode is used to perform



Figure 3.5: Pin configuration of the MAX1148 [Ma07a]

the conversion. In this mode SSTRB goes low at the start of the conversion and then goes high when the conversion is complete. To reduce noise, the serial clock stays low while SSTRB is low. With an input range equal to the fullscale range of the ADC, the effective number of bits (ENOB) is 13. Furthermore, this ADC has a sampling rate of 60 ksps.

$\operatorname{Bit}$	Name	Description
7	Start	The first logic 1 after $\overline{\text{CS}}$ goes low defines the beginning of the control byte
6	SEL2	Channel colort hits
5	SEL1	of [Me07a]
4	SEL0	c.i. [Ma07a]
3	SGL/DIF	Selects single-ended (1) or differential (0) conversions
2	UNI/BIP	Selects unipolar $(1)$ or bipolar $(0)$ conversion mode
		Selects clock and power down modes.
1	DD1	PD1 = 0 and $PD0 = 0$ selects full power-down mode
1	PD1 DD0	PD1 = 0 and $PD0 = 1$ selects fast power-down mode
0	PD0	PD1 = 1 and $PD0 = 0$ selects internal clock mode
		PD1 = 1 and $PD0 = 1$ selects external clock mode

Table 3.2: Control byte format of the MAX1148 [Ma07a]

#### 3.4.3 The Microcontroller

The converted data from the ADC has to be read out and sent to the slow-control to monitor the temperature and humidity inside the detector. For the first task a device with SPI is required, for the second task a CAN-Bus is to be used. Therefore the AT90CAN64 from Atmel Corporation [At08a], an 8-bit AVR microcontroller with 64 kB ISP flash and CAN controller, has been selected. Using the STK500 development system from Atmel, the MC can be programmed via an RS232 interface. This development board is connected to the mainboard over a AVR-ISP interface, which is described in [At08b, Fe09]. To be able to be responsive to bugs and potential changes for the monitoring it is planned to flash a CAN bootloader on the microcontroller according to [At05]. To read out the ADC the SPI clock is set to 125 kHz and the control byte is set to 1XXX1110, whereby XXX stands for the appropriate channel selection. Figure 3.6 shows a transfer between the ADC and the microcontroller.



Figure 3.6: Complete SPI transfer between the AT90CAN64 and the MAX1148. For a better view the values of CLK and MOSI are shifted.

For a better view of the individual signals, the clock and MOSI signals were shifted in this plot. One can clearly see the three bytes transferred, the control byte and two bytes with the converted data. It is planned to measure each signal several times and average them to reduce the uncertainty before transmitting the data via the CAN bus.

### 3.5 Piggyback Board for the Humidity Sensors

The humidity sensors were already described in chapter 2.2. As mentioned in that chapter they only need a +5 V power supply. The output voltage uses nearly the full range of the ADC so that no amplification of the signals is required. Therefore this PCB only consists of a voltage regulator as shown in figure 3.3a) and eight fuses. The fuses prevent a short-circuit of the humidity sensors due to radiation damage.

## 3.6 Piggyback Board for the Temperature Sensors

The main part of this board is the current source for the sensors. Different types of current sources were developed and tested. The results of these tests will be presented in the next chapter of this report. The instrumentation amplifier AD623 [AD08a] is used for amplification of the signals. This amplifier has an adjustable gain G, which can be set up with a resistor  $R_G$  between the pins 1 and 8 according to

$$G = 1 + \frac{100 \,\mathrm{k}\Omega}{R_G} \qquad . \tag{3.1}$$

It is also possible to put an offset on the signal. The amplifier itself is symmetrically supplied with  $\pm 5$  V DC. It was planned to amplify the signal by a factor of nearly 70 and subtract an offset of 5 V so that one ends up with a signal range of roughly 1.2 to 2.8 V instead of 88 to 112 mV. According to fig. 3.7 unfortunately this is not possible.



Figure 3.7: Maximum output voltage vs. common-mode input [AD08a]

Due to the design of the current source the input voltages are 2.5 and 2.6 V at 0 °C. Therefore the maximum output voltage without any offset is roughly 3.5 V. This is the reason why actually only an amplification of 30 and no offset voltage is used. This means that the signals applied to the ADC have a range of approximately 2.6 to 3.4 V. Figure 3.8 illustrates the basic design of the temperature piggyback board.



Figure 3.8: Simplified schematic of the temperature piggyback board

# 4 Test Measurements of the $THM\overline{P}$

### 4.1 Current Sources

As mentioned in chapter 2.1 the drift of the current sources should be smaller than the resolution. A measurable sensitivity of 0.05 K, which corresponds to a change of the light yield of 0.15%, is aimed for. This accords with a change of the resistance of the sensor of 0.02  $\Omega$ . Therefore the current drift should be smaller than  $2 \cdot 10^{-4} / 0.05$  K.

Overall, three different types of current sources with different variants were tested. In a climatic chamber the temperature dependencies of the sensor current were measured between -30 °C and +30 °C.

#### 4.1.1 Design Descriptions

Figure 4.1 shows the schematics of the three types of current sources.



Figure 4.1: The three tested versions of the current source

The first version consists of a 2.5 V reference, a 2.5 k $\Omega$  precision resistor and an operational amplifier. During development, attention was focused on the temperature drift of these devices. This source was developed and tested with different parts. For the voltage reference the ADR441 from Analog Devices [AD08c] and the LTC6652 from Linear Technology [LT08b] were used, and for the operational amplifier the OPA4241 and the AD8554 from Analog Devices [AD08b] were used. The voltage drops across the 2.5 k $\Omega$  resistance to provide the needed current of 1 mA. The current is applied to the temperature sensor as well as to the non-inverting input of the amplifier, whose output is attached to the virtual ground of the reference and the inverting input. This circuit stabilizes the reference and is self-adjusting.

The second version (fig. 4.1b) is pretty similar to the first one. The reference voltage is attached to the non-inverting input of the amplifier. The sensor has to be connected to the output and fed back to the inverting input. The advantage of this design is, that one can build multiple current sources with only one voltage reference. This design was only tested with the LTC6652 and AD8554.

The third current source is a Wilson current mirror. Like the other two versions, this one contains a 2.5 V reference and a high precision 2.5 k $\Omega$  resistor to provide the current of 1 mA. According to literature (e.g. [Ti02]), this current mirror is a very stable current source.

#### 4.1.2 Test Results

To study the temperature drift of the sources each developed version was operated in a climatic chamber. The sensor current was measured every 20 seconds with the Agilent 34980A digital multimeter [Ag09]. Per test measurement, 61 temperature values from  $-30 \,^{\circ}\text{C}$  to  $+30 \,^{\circ}\text{C}$ , were recorded with a step size of one degree Celsius at intervals of one hour. This means that 180 values are measured per temperature step. Due to temperature variation caused by control hysteresis of the climatic chamber and not yet constant temperatures of the ICs, the first 30 values were not taken into account. The remaining values were filled into a histogram and a Gauß distribution was fitted to them. The mean of the distribution is plotted against the respective temperature. Figure 4.2 shows the results of these measurements. Since the versions with the ADR441 and the OPA4241 delivered nonsatisfying results, they are not considered here.



Figure 4.2: Sensor current vs temperature

Contrary to expectations the Wilson current mirror has the largest drift with  $7 \cdot 10^{-4}$  mA/K. Also the sensor current drift of this source is lower than was requested (cf fig. 4.2c). The first and second versions show a drift of roughly  $2 \cdot 10^{-5}$  mA/K (fig. 4.2a and b). Regarding the operational point at T = -25 °C version 2 has nearly a constant current output. Therefore this version which consists of fewer parts, will be used for the THMP. It remains to be said that this version was tested with a precision resistor, which has a temperature drift of only 0.2 ppm/K, and a 2.49 k $\Omega$ resistor with 10 ppm/K. No significant difference in the behavior of the source has been observed during the measurements, neither with the high precision resistor nor with the one with 10 ppm/K. Therefore, the resistor with 10 ppm/K is being used.

### 4.2 Temperature Cycles

One aim of development was the proper operation of the  $\mathsf{THMP}$  in a temperature range from  $-30\,^{\circ}\mathrm{C}$  to  $+30\,^{\circ}\mathrm{C}$ . In order to check this, a prototype of the  $\mathsf{THMP}$  was installed in the climatic chamber. One humidity and one temperature piggyback board were attached to the mainboard, each with two sensors. For evaluating the self-constructed temperature sensors a commercial PT100 was used as second sensor. Analogous to the tests of the current sources, 61 temperature steps at an intervall of one hour each, were measured. The data from the ADC was read out with an AVR32ngw100



Figure 4.3: Prototype of the THMP. The figure shows the mainboard with one humidity and one temperature piggyback board attached.

from Atmel. Nothing unusual was noted in these measurements; for further information please refer to [Fr09].

### 4.3 Measurements in a Magnetic Field

In case the  $\mathsf{THMP}$  is placed inside the cooled area of the EMC it has to work properly in a magnetic field of 1.3 T for the endcap and 2.5 T for the barrel part. First tests with the aforesaid prototype were carried out in a magnetic field of 1.5 T. In figure 4.4 the measured values are plotted against the time. During the first four minutes, in which the magnetic field increases from 0 to 1.5 T, no effects were observed.



Figure 4.4: Temporal development of the sensor output in a magnetic field. a) measured RH of two humidity sensors, b) measured voltage output of two temperature sensors.

In the following three minutes, the angle between the magnetic field and the  $\mathsf{THMP}$  including the sensors was varied between 0° and 90°, and in the last minute the magnetic field was shut down. The small decrease of the RH and the increase of the temperature respectively, are caused by the heating-up of the magnet. As no unexpected effects appeared during this test, one can assume

that the  $\mathsf{THMP}$  will work in a magnetic field of up to 1.5 T. However, the behavior of the  $\mathsf{THMP}$  remains to be tested in a magnetic field of 2.5 T and also if the magnet quenches.

### 4.4 Studies of Radiation Hardness

The irradiation tests were carried out at Gießen Irradiation Facility, where the THMP was irradiated with a  $^{60}$ Co-source, which emits two photons with an energy of 1.17 and 1.33 MeV by its decay. The source has an activity of  $10^{13}$  Bq, thus a rate of about 210 Gy/h at a distance of 20 cm can be achieved [No09]. This rate is by far higher than the highest dose expected in the PANDA detector. Like for the measurements in the magnetic field, two sensors were attached to each piggyback board and the ADC was read out every second with the AVR32ngw100. In the first test, the THMP was exposed to 660 Gy. The signals of the humidity sensors linearly increased immediately after starting the irradiation, while the voltage output of the temperature sensors remaind nearly constant. Approximately after 1.75 h the signals of all four sensors began to rise exponentially until they reached the overflow of the ADC. The output voltage of the temperature sensors broke down after 2.7 h, which led to the assumption that one or more parts were not radiation-hard. When measuring the voltages on the PCB it turned out, that the used voltage regulator MIC5200 from Micrel [Mi05] was damaged and put through the supply voltage of 6 V DC. Further tests showed that the output voltage rose linearly with the accumulated dose until it reached the supply voltage. The observed effects can be explained by the changing supply voltage [Fr09].

For a second test, in which nearly 500 Gy were accumulated, the voltage regulator LT1129 [LT08a] was used and withstood the radiation. However, the -5 V DC regulators of type LT1175 [LT05] were destroyed, but the exact dose was not monitored. This is why more tests will be done to determine the upper limit of the radiation dose the parts can withstand.

In a third radiation test another positive voltage regulator was irradiated. A total dose of 720 Gy was accumulated in the LP3962 from National Semiconductor [Na06]. No changes during or after the irradiation could be observed, so that this type of positive voltage regulator will be used for the THMP. This regulator is also employed by the GSI for the ASICs [Wi09].

Further tests were carried out and showed that the HIH-4000 humidity sensor are radiation-hard up to 530 Gy.

	1	1 [0] ]
part	description	dose [Gy]
$MIC5200^1$	positive low drop-out regulator	$\sim 0$
$LT1129^{1}$	positive low drop-out regulator	> 500
LP3962	positive low drop-out regulator	> 720
$LT1175^{2}$	negative low drop-out regulator	$\sim 680$
LTC6652	voltage reference	> 1160
AT90CAN64	8-bit AVR MC	> 720
MAX1148	14-bit ADC	> 1160
MAX4581	8:1 MUX	> 1160
AD8554	quad OpAmp	> 1160
AD623	instrumentation amplifier	> 1160
PCA82C250	CAN transceiver	> 1160
HIH-4000	humidity sensor	$\sim 530$
self-constructed PT100	temperature sensor	> 1160

Table 4.1: Maximum tested dose for each part it withstood at a rate of 210 Gy/h

<sup>&</sup>lt;sup>1</sup>No longer in use.

<sup>&</sup>lt;sup>2</sup>The radiation hardness of this device will be tested again.

# 5 Summary and Outlook

The  $\mathsf{THMP}$  was developed for monitoring the temperature and humidity inside the EMC. Therefore, proper operation in the cooled area has to be guaranteed. Different tests were done to ensure that the  $\mathsf{THMP}$  works reliably in this environment.

The radiation hardness of all components was tested with a dose of at least 720 Gy. The temperature dependencies of the individual parts are beneath their critical limits. The proper operation in a magnetic field of up to 1.5 T is also assured.

In future, one has to check if the  $\mathsf{THMP}$  will also work in a magnetic field of 2.5 T. Moreover, the radiation hardness of the negative voltage regulator for the  $\mathsf{THMP}$  has to be studied in more detail. This will be done soon at Gießen Irradiation Facility. Furthermore it is considered to replace the instrumental amplifier of the temperature piggyback board to enlarge the signal range applied to the ADC.

Meanwhile two test samples of the mainboard have been produced with the final four-layer design. A first test of the whole  $\mathsf{THMP}$  in a realistic environment will be done with the PROTO192 prototype of the forward endcap. Four to five boards will monitor the temperature and humidity inside this prototype to ensure that the cooling, insulation and nitrogen flushing are performing as expected.

**ADC** Analog to Digital Converter

 $\boldsymbol{\mathsf{C}} \ \mathrm{Capacitance}$ 

 ${\sf CAN}\,$  Controller Area Network

**CMOS** Complementary Metal Oxide Semiconductor

**EMC** Electromagnetic Calorimeter

**ENOB** Effective Number of Bits

 $\ensuremath{\mathsf{FAIR}}$  Facility for Antiproton and Ion Research

**GSI** Gesellschaft für Schwerionenforschung

 $\ensuremath{\mathsf{HEP}}$  High Energy Physics

**HESR** High Energy Storage Ring

J-FET Junction Field-Effect Transistor

 ${\sf L}$  Inductance

**LED** Light Emitting Diode

 $\boldsymbol{\mathsf{LY}}$  Light Yield

 $\textbf{MC} \ \mathrm{Microcontroller}$ 

**MISO** Master Input, Slave Output

**MOSI** Master Output, Slave Input

 $\ensuremath{\mathsf{MSOP}}$  Micro Small-Outline Package

**MUX** Multiplexer

 ${\sf N.C.}$  not connected

**PANDA** Pbar Annihilation at Darmstadt

**PCB** Printed Circuit Board

**PWO** Lead Tungstate

 ${\boldsymbol{\mathsf{R}}}$  Resistance

**RH** Relative Humidity

 ${\boldsymbol{\mathsf{RT}}}$  Room Temperature

 $\textbf{SMD} \ \text{Surface-Mounted Device}$ 

**SOP** Small-Outline Package

 $\textbf{SOT} \ \text{Small-Outline Transistor}$ 

 ${\bf SS}\,$  Slave Select

**SPI** Serial Peripheral Interface

**THMP** Temperature and Humidity Monitoring Board for PANDA

**TSSOP** Thin-Shrink Small Outline Package

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Bottom side







# Bottom side



