A Test System for the Front-End Electronics of the PANDA MVD

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Alessandra Lai, Forschungszentrum Jülich, 15 March, 2016
The ČPANDA Experiment
Antiproton Annihilation at Darmstadt

- fixed p/N target
- $\bar{p}$ beam 1.5 - 15 GeV/c
MVD: Micro Vertex Detector

- event rate \( \approx 20 \text{ MHz} \)
- vertex resolution \(< 100 \mu\text{m}\)

- time resolution \( \approx 10 \text{ ns} \)
- deposited energy information

- four barrel layers in the center
- six disk layers in the fw direction
- pixel detectors in the inner part
  \( \rightarrow \) front-end chip: ToPix
- double-sided strip detectors in the outer part
  \( \rightarrow \) front-end chip: PASTA [HK 7.2]

High performance and flexible test system needed for ToPix and PASTA
The basic components

- **Data conversion & communication with PC:**
  - DUT: ToPix, PASTA
  - evaluation board: Xilinx ML605 (Virtex-6 FPGA)
  - firmware: VHDL

- **Configuration & data handling:**
  - PC
  - software: C++
  - MVD readout framework (MRF)
  - Qt-based GUI
JDRS: Jülich Digital Readout System

The real components
MRF: MVD Readout Framework

Four abstraction layers isolate low level from higher level functions:

- **physical layer**
  → ethernet connection between ML605 and PC

- **generic access layer (GAL)**
  → data transfer and formatting
  e.g. open a connection, send and receive data packages, ...

- **transport access layer (TAL)**
  → board-specific functions
  e.g. the clock generation, flush of buffers, ...

- **chip access layer (CAL)**
  → DUT-specific functions
  e.g. configuration and data readout, ...
Qt-based GUI

Make it modular by separating the functionalities in independent projects.

Caveat

Communication between projects is needed.

One main window that contains all the other tabs as sub-widgets.

```cpp
connect(ui->pushButton_defaultMode, SIGNAL(clicked()),
       ui->widget_UDP, SLOT(connectUDP()));
```
Load and save settings

Settings (e.g. configuration data) are stored in `.json` files → Qt offers support for JSON

Advantages:
- easy programmatic access to the key-value pairs,
- human readable format.

```json
"CommandCCR0" : 32,
"CommandCCR1" : 33,
"CommandCCR2" : 34,
"CounterMode" : 1,
"CounterEnable" : 1,
"ReadoutCycleHalfSpeed" : 1,
"FreezeStop" : 4,
"Leak_P" : 1,
"SelectPol" : 1,
"PreEmphasisTimeStamp" : 1,
"PreEmphasisCommands" : 1,
"CounterStopValue" : 4095

"CalLevelDac" : 5000,
"VCasIlc" : 45580,
"VCasIfb" : 40350,
"VRefBaseline" : 37600,
"notUsed1" : 0,
"notused2" : 0,
"VRefD" : 37300,
"VCasD" : 32450
```

A similar format is available for the pixel configuration as well:
- the key is the row number ($n_{Keys} = n_{Row} = 20$),
- the value is a 32-entry array ($n_{Col} = 32$).
Data handling

The data buffer is read (and emptied) when needed and the data is stored in a file. Two possibilities for data storage exist:

- (direct) ASCII,
- boost serializer.

The interface allows to add new methods for storing the data (e.g. FairMQ, useful for beam tests).
One part of the GUI is dedicated to measurements → characterization of ToPix.

Each pixel has an internal circuit for the injection of a certain amount of charge. Two modes of operation:

- inject once into enabled pixels
- enable certain pixels (pattern), inject and repeat the pattern shifted by one row for \( n \) times

Up to two parameters can be swept during the charge injection (e.g. threshold parameters, ...)

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Measurements: online visualization

Row 1, 16, 31 enabled.
The JDRS is under version control on Git (firmware and software).
- JDRS core (git submodule)
- JDRS ToPix

The core repository contains all the JDRS basic functionalities (UDP connection, register access, chip configuration, ...).
\[\Rightarrow\] 100\% reusable for PASTA.

The ToPix repository contains ToPix specific functionalities.
\[\Rightarrow\] partially reusable for PASTA (adaptations are required).

It is sufficient to checkout the repo and run the JDRS (config file included).
Dependencies:
- boost library
- root
Summary

- The PANDA MVD will use pixel and strip detectors.
- Two front-end chips: ToPix and PASTA.
- The JDRS is under development.

Requirements on the system: **modular, maintainable, flexible** and **user friendly**. Achieved by:
  - arranging the code to match the GUI structure,
  - making the system as independent as possible from the environment,
  - reducing the dependencies on external libraries/packages,
  - optimizing data handling and storage.

Preparation for full characterization of the chip (at present ToPix, in a later stage PASTA) with automatic routines that can be handled from the GUI.
References

- ToPix ASIC: INFN Torino
- PASTA ASIC: A. Zambanini, PhD thesis [http://hss-opus.ub.ruhr-uni-bochum.de/opus4/frontdoor/index/index/docId/466]
- ASICs pictures: courtesy of Daniela Calvo and Tommaso Quagli
- ML605: http://www.xilinx.com/
Qt - 'cute' framework

Qt is a widely used framework for developing application software with graphical user interfaces (GUIs) but not only (e.g. command-line tools).

- open source
- cross-platform (Linux, Windows, Android, Mac, ...)
- uses system resources (i.e. the app gets a native look)
- supports standard C++
- signals and slots mechanism (for event handling)
- supports several compilers (e.g. GCC, Visual studio)
- supports threading for parallel programming
- supports a designer for the layout of the UI
- ...

connect(this, SIGNAL(sendTopix4CtrlPtr(TMrfCal_Topix4 *)),
        ui->widget_chipConfig, SLOT(recvTopix4CtrlPtr(TMrfCal_Topix4 *)));
connect(ui->pushButton_defaultMode, SIGNAL(clicked()),
        ui->widget_UDP, SLOT(connectUDP()));
Former version

Originally designed for beam test measurements → suffered from strict time constraints, quick implementation and workarounds:

- environment dependency
- lack of modularity and flexibility
- lack of structure
- hard coded settings and magic numbers in the code
- extreme sensitivity to external changes
- cumbersome for inexperienced users

- max reliable readout frequency ≈ 50 MHz
- data from chip handled by *FairMQ* → cumbersome and not necessary for lab measurements

The code to generate the whole GUI in a single file ≈ 2000 lines!
Refactoring process: strategy

The idea is to make the existing framework modular by separating the functionalities in independent projects. Each project consists of, at least:

- a .pro file
- a standard C++ class
- a form (i.e. the actual UI)

Rule of thumb: one project per tab.

Caveat

Communication between projects is needed (e.g. an event that occurs in one tab might trigger an instruction in another tab).

One main window that contains all the other tabs as sub-widgets.
Main window and widgets

Each widget:
- is an independent project that can be executed standalone
- is included in the main window to build up the full gui
- is connected to the others, if needed, through the main window

The main window:
- holds the sub-widgets
- performs the connections between signals and slots
- initiates and distributes the global information
Widgets

**UDP Connection**
- ML605 board: IP address: 192.168.0.2, Port: 50000
- This PC/Notebook (MainSoftware): IP address: , Port Slow Control: 50000

**Check Connection**
Send a ping command and check for a pong response via slow control socket.

**Register Access**
- Single Registers
  - Address: Dx
  - Value: Dx

**Bulk Register Read**
- Words to read: 1
- Result: 0 words read

**Chip Configuration**
- Write and read configuration

**Debug**
- Enable debug mode
- Disable debug mode
- Read CCR0
- Read CCR1
- Read CCR2

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The MRF

The MRF is now a library that can be built independently. It has its own `.pro` file but no UI.

→ replace the files 'include' with the library include

```cpp
SOURCES += main.cpp
mainwindow.cpp
  topix4_fairmq_readout.cpp
  .. writetofile.cpp
  ../MRF/source/mrfdata_chain2ltc2604.cpp
  ../MRF/source/mrfdata_chainltc.cpp
  ../MRF/source/mrfdataadv2d.cpp
  ../MRF/source/mrfdataadvbase.cpp
  ../MRF/source/mrfcal_topix4.cpp
  ../MRF/source/mrfcal.cpp
  ./MRF/source/mrftal_rbtotipx4.cpp
  ./MRF/source/mrftal_rbbase_v6.cpp
  ./MRF/source/mrftal_rbbase.cpp
  ./MRF/source/mrfdataregaccess.cpp
  ./MRF/source/mrfdataadv1d.cpp
  ...

HEADERS += mainwindow.h
  topix4_fairmq_readout.h
  ../writetofile.h
  ...
```

LIBS +=

- $\text{PROJECTPATH}/JDRS\_core/MRF/\text{lib}$ -lMRF
One way to check if the process of writing to the chip was successful is to read back the data coming from it.

All the data that is written to the chip (configuration values, pixel status, etc) can be read back and visualized in the GUI.

For example the pixel mask status.
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